UM1461
User manual

STM3240G-EVAL evaluation board

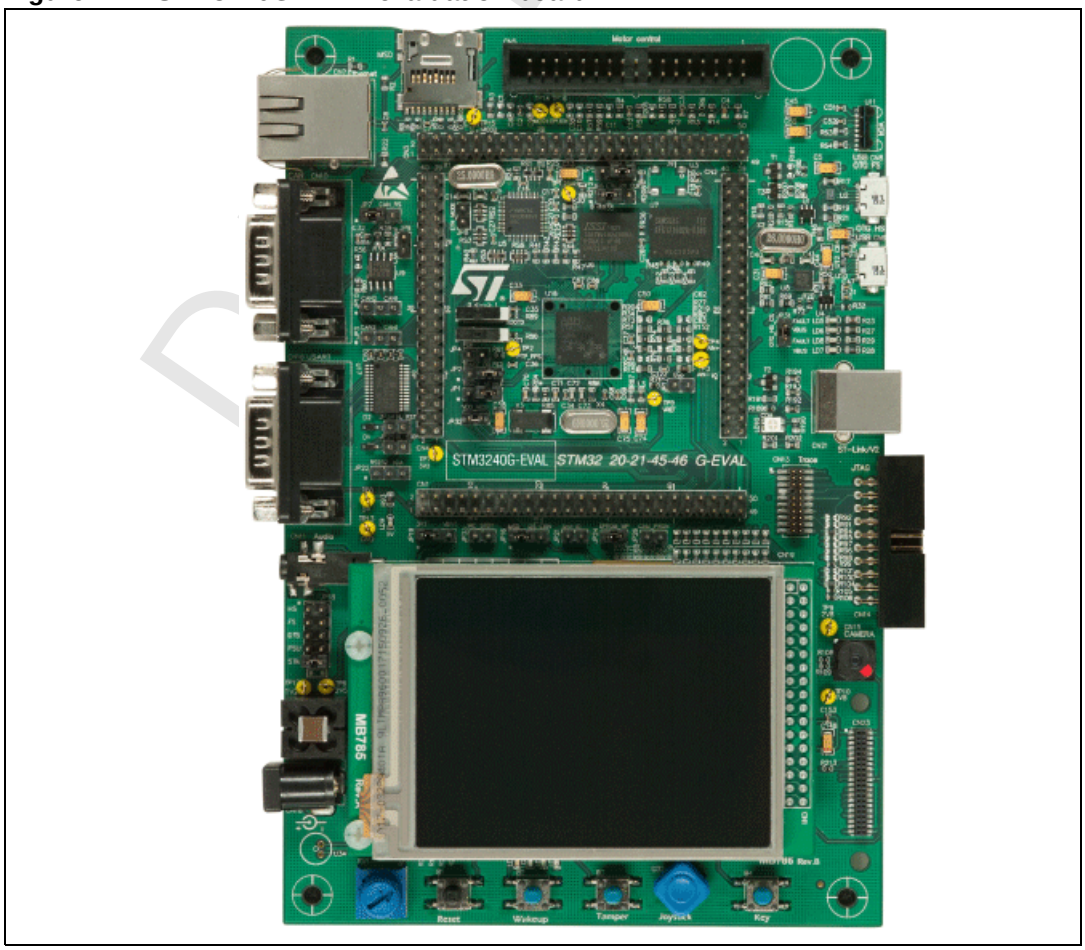
Introduction

The STM3240G-EVAL evaluation board is a complete demonstration and development platform for the STM32 F-4 series and includes an embedded STM32F407IGH6 high-performance ARM®Cortex™-M4 32-bit microcontroller.

The full range of hardware features on the board is provided to help you evaluate all peripherals (USB OTG HS, USB OTG FS, ethernet, motor control, CAN, MicroSD card, smartcard, USART, Audio DAC, RS-232, IrDA, SRAM, MEMS, EEPROM… etc.) and develop your own applications. Extension headers make it possible to easily connect a daughter board or wrapping board for your specific application.

The in-circuit ST-LINK/V2 tool can be easily used for JTAG and SWD interface debugging and programming.

Figure 1. STM3240G-EVAL evaluation board



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1 Overview

1.1 Features

- STM32F407IGT6 microcontroller. The MCU marking on the first boards manufactured is STM32F457IGH6.
- 16 Mbit SRAM
- 1 Gbyte or more MicroSD card
- Boot from user Flash, system memory or SRAM
- Both ISO/IEC 14443 type A and B smartcard support
- I^2C compatible serial interface 8 Kbytes EEPROM, MEMS and I/O expander
- IEEE 802.3-2002 compliant ethernet connector
- Two CAN 2.0 A/B channels on the same DB connector
- RS-232 communication
- IrDA transceiver
- USB OTG (HS and FS) with Micro-AB connector
- Inductor motor control connector
- I^2S Audio DAC, stereo audio jack for headset
- 3.2" 240x320 TFT color LCD with touchscreen
- 4 color LEDs
- Camera module and extension connector for ST camera plug-in
- Joystick with 4-direction control and selector
- Reset, wakeup, tamper and user button
- RTC with backup battery
- Extension connector for daughterboard or wrapping board
- JTAG, SWD and trace debug support
- Embedded ST-LINK/V2
- Five 5V power supply options: Power jack, USB FS connector, USB HS connector, ST-LINK/V2 or daughterboard

1.2 Demonstration software

Demonstration software is preloaded in the board's Flash memory for easy demonstration of the device peripherals in standalone mode. For more information and to download the latest version, please refer to STM3240G-EVAL demonstration software available on web: www.st.com/mcu

1.3 Order code

To order the STM32F407IGT6 MCU evaluation board, use order code STM3240G-EVAL.

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1.4 Delivery recommendations

Several verifications are needed before using the board for the first time to make sure that nothing has been damaged during shipment and no components are unplugged and lost.

When the board is extracted from its plastic bag, please check that no component remains in the bag. Main components to verify are:

1. The 25MHz crystals (X1 and X4) may have been removed by a shock.
2. The camera connected on socket CN15 located on the right side of the board under the JTAG connector may be unplugged. If this is the case, please refer to the note in *Section 2.18: Camera module* to make sure to replug it in the correct position.
3. The MicroSD card may have been ejected from its connector CN6 (top left corner of the board).

The plastic protection on the camera should be removed carefully as the connection is very fragile.

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2 Hardware layout and configuration

The STM3240G-EVAL evaluation board is designed around the STM32F407IGH6 in the UFBGA176 package. The hardware block diagram *Figure 2* illustrates the connection between STM32F407IGH6 and peripherals (Camera module, LCD, SRAM, EEPROM, MEMS, USART, IrDA, USB OTG HS, USB OTG FS, Ethernet, Audio, CAN bus, Smart card, MicroSD card and motor control) and *Figure 3* will help you locate these features on the actual evaluation board.

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Figure 2. Hardware layout and configuration

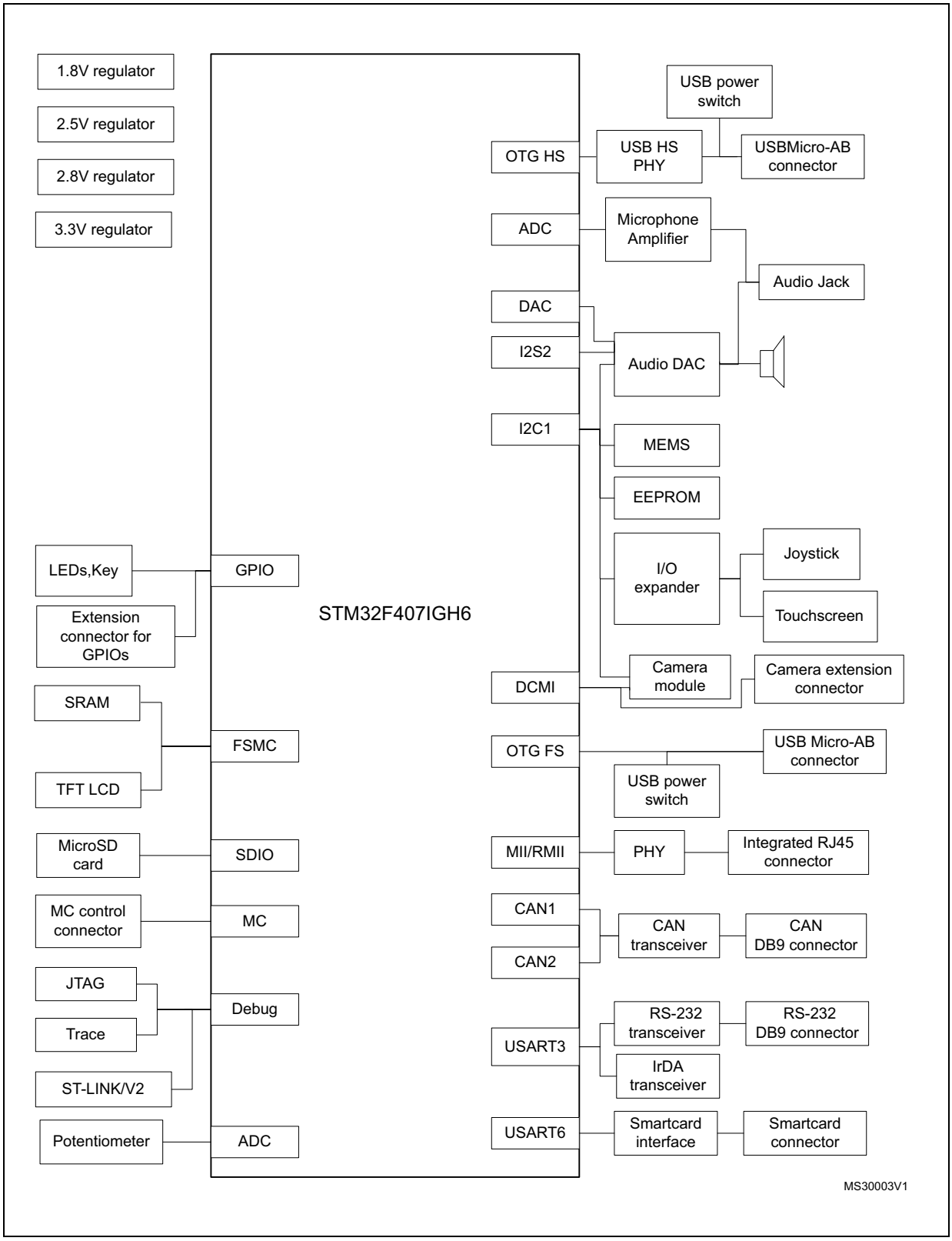
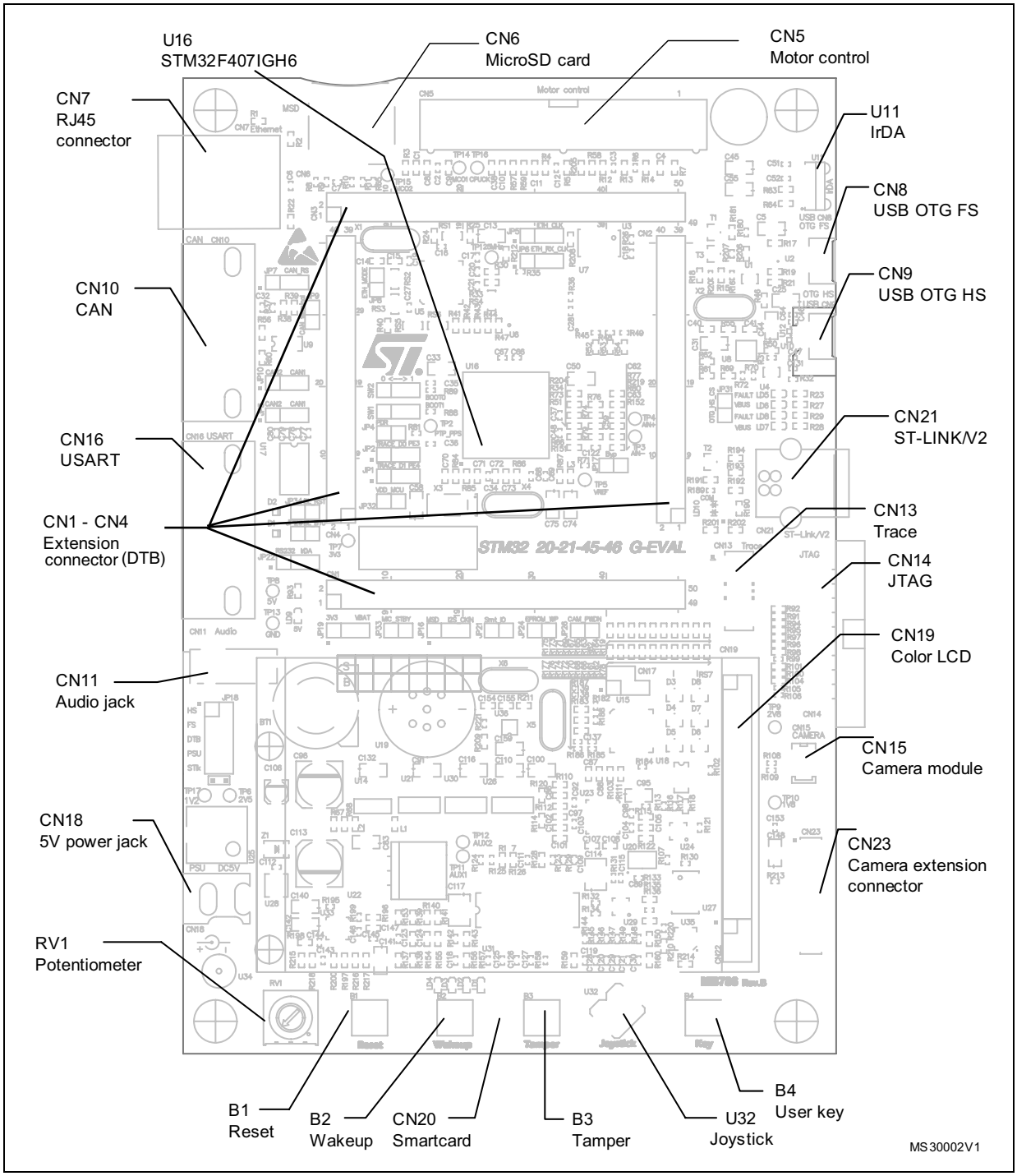


Figure 3. STM3240G-EVAL evaluation board layout



2.1 Power supply

STM3240G-EVAL evaluation board is designed to be powered by 5V DC power supply and to be protected by PolyZen from a wrong power plug-in event. It is possible to configure the evaluation board to use any of following five sources for the power supply:

- 5V DC power adapter connected to CN18, the Power Jack on the board
- 5V DC power with 500 mA limitation from CN8, the USB OTG FS Micro-AB connector
- 5V DC power with 500 mA limitation from CN9, the USB OTG HS Micro-AB connector
- 5V DC power with 500 mA limitation from CN21, the ST-LINK/V2 USB connector
- 5V DC power from both CN1 and CN3, the extension connector for daughterboard (DTB for daughterboard on silkscreen)

The power supply is configured by setting the related jumpers JP4, JP32, JP18 and JP19 as described in *Table 1*.

Table 1. Power related jumpers and solder bridges

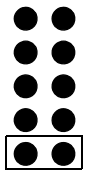
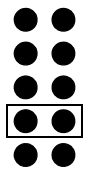
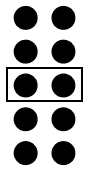
Jumper	Description
JP4	JP4 should be fitted to enable power down reset (PDR). PDR is disabled when JP4 is not fitted. Default setting: Fitted.
JP32	MCU_VDD is connected to 3.3V power when JP32 is closed and MCU current consumption measurement can be done manually by multi-meter when JP32 is open. Default setting: Fitted.
JP18	JP18 is used to select one of the five possible power supply sources. To select the **ST-LINK/V2 USB connector** (CN21) power supply, set JP18 as shown: (Default setting) HS FS DTB PSU STlk 
	To select **power supply jack** (CN18) power supply, set JP18 as shown: HS FS DTB PSU STlk 
	To select **daughterboard connector** (CN1 and CN3) power supply, set JP18 as shown: HS FS DTB PSU STlk 

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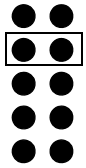
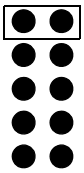
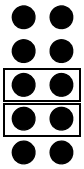
Table 1. Power related jumpers and solder bridges (continued)

Jumper	Description		
JP18 (cont.)	To select **USB OTG FS** (CN8) power supply, set JP18 as shown:	HS FS DTB PSU STlk	
	To select **USB OTG HS** (CN9) power supply, set JP18 as shown:	HS FS DTB PSU STlk	
	To select **power supply jack** (CN18) power supply to both STM3240G-EVAL and daughterboard connected on CN1 and CN3, set JP18 as shown (**daughterboard must not have its own power supply connected**)	HS FS DTB PSU STlk	
JP19	To connect Vbat to the battery, set JP19 as shown:		
	To connect Vbat to 3.3V power, set JP19 as shown: (Default setting)		

Note: The LED LD9 is lit when the STM3240G-EVAL evaluation board is powered by the 5V correctly.

2.2 Boot option

The STM3240G-EVAL evaluation board is able to boot from:

- Embedded User Flash
- System memory with boot loader for ISP
- Embedded SRAM for debugging

The boot option is configured by setting switch SW1 (BOOT1) and SW2 (BOOT0). The BOOT0 can be configured also via RS-232 connector CN16.

Table 2. Boot related jumpers

BOOT 0	BOOT 1	Boot source
0	Don't care	STM3240G-EVAL boots from **User Flash** (Default setting)
1	1	STM3240G-EVAL boots from **Embedded SRAM**
1	0	STM3240G-EVAL boots from **System Memory**

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2.3 Clock source

Four clock sources are available on STM3240G-EVAL evaluation board for STM32F407IGH6 and RTC embedded:

- X1, 25 MHz crystal for ethernet PHY with socket. It can be removed when clock is provided by MCO pin of the MCU
- X2, 26 MHz crystal for USB OTG HS PHY
- X3, 32 kHz crystal for embedded RTC
- X4, 25 MHz crystal with socket for STM32F407IGH6 microcontroller (It can be removed from socket when internal RC clock is used.)

2.4 Reset source

The reset signal of STM3240G-EVAL evaluation board is low active and the reset sources include:

- Reset button B1
- Debugging tools from JTAG connector CN14 and trace connector CN13
- Daughterboard from CN3
- RS-232 connector CN16 for ISP
- ST-LINK/V2

2.5 Audio

The STM3240G-EVAL evaluation board enables stereo audio play and microphone recording by an external headset connected on audio jack CN11. An audio DAC CS43L22 is connected to both I2S2 port and one DAC channel while a microphone amplifier is connected to the ADC of STM32F407IGH6. The CS43L22 can be configured via I2C1 and the external PLL (U36) can be used to provide external clock which is connected to I2S_CKIN pin (PC9).

Table 3. Audio related jumpers

Jumper	Description
JP16	Description of JP16 is in *Table 10 on page 15*.
JP33	The microphone amplifier can be disabled when JP33 is fitted. Default setting: Not fitted

2.6 EEPROM

A 64KBit EEPROM is connected to the I2C1 bus of STM32F407IGH6.

Table 4. EEPROM related jumper and solder bridge

Jumper	Description
JP24	The EEPROM is in Write Protection mode when JP24 is fitted. Default Setting: Not fitted

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2.7 CAN

STM3240G-EVAL evaluation board enables two channels of CAN2.0A/B compliant CAN bus communication based on a 3.3V CAN transceiver on one DB9 connector (CN10). The two CAN buses can be disconnected by jumpers from relevant STM32F407IGH6 I/Os which are shared with FSMC and USB OTG HS. Jumpers JP3 and JP10 must be refit to enable CAN1 or CAN2 as listed in *Table 5*.

High-speed, Standby and Slope Control modes are available and can be selected by setting jumper JP7.

Table 5. CAN-related jumpers

Jumper	Description
JP3	To connect CAN1_TX to CAN transceiver, set JP3 as shown:
	To connect CAN2_TX to CAN transceiver, set JP3 as shown:
JP10	To connect CAN1_RX to CAN transceiver, set JP10 as shown:
	To connect CAN2_RX to CAN transceiver, set JP10 as shown:
	PD0 and PB5 are disconnected from the CAN transceiver and used for FSMC and USB_OTG_HS when jumper JP10 is not fitted (default setting).
JP7	To enable the selected CAN transceiver to work in Standby mode, set JP7 as shown:
	To enable the selected CAN transceiver to work in High-speed mode, set JP7 as shown (default setting):
	To enable the selected CAN transceiver to work in Slope Control mode, do not fit a jumper on JP7.
JP9	To enable the terminal resistor for the selected CAN, fit a jumper on JP9. (Default setting: not fitted)

2.8 RS-232 and IrDA

Both RS-232 and IrDA communication is enabled by D-type, 9-pin RS-232 connectors (CN16) and IrDA transceiver U11 which are connected to USART3 of STM32F407IGH6 on STM3240G-EVAL evaluation board.

For ISP support, two signals are added on the RS-232 connector CN16:

- Bootloader_RESET (shared with CTS signal)
- Bootloader_BOOT0 (shared with DSR signal)

RS-232 or IrDA can be selected by setting of JP22 and ISP can be enabled by setting of jumper JP29 and JP34.

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Table 6. RS-232 and IrDA related jumper

Jumper	Description
JP22	To connect USART3_RX to IrDA transceiver and enable IrDA communication, set JP22 as shown:
	To connect USART3_RX to RS-232 transceiver and enable RS-232 communication, set JP22 as shown (Default setting):
	To enable MicroSD card, which shares same I/Os with RS-232, JP22 is not fitted.
JP29	Bootloader_BOOT0 is managed by pin 6 of CN16 (RS-232 DSR signal) when JP29 is closed. This configuration is used for boot loader application only. Default setting: Not fitted.
JP34	Bootloader_RESET is managed by pin 8 of CN16 (RS-232 CTS signal) when JP34 is fitted. This configuration is used for boot loader application only. Default setting: Not fitted.

2.9 Motor control

STM3240G-EVAL evaluation board enables a three-phase brushless motor control via a 34-pin connector (CN5), which provides all required control and feedback signals to and from the motor power-driving board. Available signals on this connector include emergency stop, motor speed, 3-phase motor current, bus voltage, heatsink temperature coming from the motor driving board and 6 channels of PWM control signal going to the motor driving circuit.

The solder bridge (SB18) allows to choose two kinds of synchronization methods for PFCs (Power Factor Correction) while the SB17 can be set for different signals on pin 31 of CN5.

The I/O pins used on motor control connector CN5 are multiplexed with some peripherals on the board; either motor control connector or multiplexed peripherals can be enabled by the setting of solder bridges SB10, SB11, SB12, SB14, SB15 and SB16.

Table 7. Motor control solder bridges

Solder Bridge	Description	Multiplexed peripherals
SB18	When closed, SB18 redirects the PFC synchronized signal to the timer 3 input capture pin 2 in addition to the timer 3 external trigger input. Default setting: Open	
SB17	For CN5 encoder signal input (pin 31), SB17 must be open. For CN5 special motor analog signal input (pin 31), SB17 must be closed. Default setting: Open	
SB16	To connect MC_EmergencySTOP to PI4, close SB16. Default setting: Open	Camera module connected to CN15

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Table 7. Motor control solder bridges (continued)

Solder Bridge	Description	Multiplexed peripherals
SB10	To connect MC_EnIndex to PB8, close SB10. Default setting: Open	Ethernet
SB11	To connect MC_CurrentA to PC1, close SB11. Default setting: Open	
SB12	To connect MC_CurrentB to PC2, close SB12. Default setting: Open	
SB14	To connect MC_EnB to PD13, close SB14. Default setting: Open	FSMC
SB15	To connect MC_EnA to PD12 close SB15. Default setting: Open	

Note: 1 Some 0 ohm resistors have to be removed or soldered to enable motor control application except the solder bridges configurations mentioned above:

– R34, R58 & R51 to be removed

– R66, R204 & R205 to be soldered

2 MicroSD card must be removed from CN6 for motor control application.

2.10 Smartcard

STMicroelectronics smartcard interface chip ST8024 is used on STM3240G-EVAL board for asynchronous 3V and 5V smartcards. It performs all supply protection and control functions based on the connections with STM32F407IGH6 listed in *Table 8*:

Table 8. Connection between ST8024 and STM32F407IGH6

Signals of ST8024	Description	Connect to STM32F407IGH6
5V/3V	Smartcard power supply selection pin	PH15
I/OUC	MCU data I/O line	PC6
XTAL1	Crystal or external clock input	PG7
OFF	Detect presence of a card, MCU interrupt, share same pin with motor controller	PF6
RSTIN	Card reset input from MCU	PF7
CMDVCC	Start activation sequence input (Active Low), share same pin with I2S DAC and Motor control	PG12

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Smartcard shares some I/Os with I2S bus for Audio. Some jumper settings need to be reconfigured to enable smartcard as indicated below:

Table 9. Smartcard related jumper

Jumper	Description
JP21	To connect Smartcard_IO to PC6, JP21must be fitted. JP21 must not be fitted for Audio DAC connection to I2S. Default setting: Not Fitted

2.11 MicroSD card

The 1 GByte or more MicroSD card connected to SDIO of STM32F407IGH6 is available on the board. MicroSD card detection is managed by the standard I/O port PH13. MicroSD card shares I/Os with motor control, RS-232 and audio. The jumpers JP22 and JP16 must be refit and motor control connector (CN5) must be disconnected for MicroSD card function.

Table 10. MicroSD card related jumpers

Jumper	Description
JP22	Description of JP22 is in *Section 2.8: RS-232 and IrDA*
JP16	PC9 is connected to MicroSDCard_D1 when JP16 is set as shown to the right: (Default setting):
	PC9 is connected to I2S_CKIN when JP16 is set as show to the right:

2.12 MEMS

A ST MEMS device LIS302DL is connected to I2C1 bus of STM32F407IGH6 on the board.

2.13 Potentiometer

There is one 10K ohm potentiometer RV1 connected to PF9 of STM32F407IGH6 on the board.

2.14 ADC

Two test points (TP3 AIN-) and (TP4 AIN+) are placed close to port PC1 of the MCU allowing precise measurements on ADC1, ADC2 or ADC3 channel 11. As PC1 is also used as current A input on the motor control connector it is recommended to remove R219 to optimize noise immunity on this input.

A potentiometer RV1 is connected to PF9 of STM32F407IGH6. If needed, a low pass filter (R74 and C59) can be placed on this input to reduce the bandwidth of the analog input PF9.

It is also possible to place the Ethernet PHY (U5) in low power mode in order to reduce the noise induced by this high frequency peripheral. Power down pin (MII_INT in the schematic) is connected to PB14 of the MCU, so this I/O can be configured as output low during analog precision measurement.

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2.15 USB OTG FS

STM3240G-EVAL evaluation board enables USB OTG full speed communication via a USB Micro-AB connector (CN8) and USB power switch (U1) connected to VBUS. The evaluation board can be powered by this USB connection at 5V DC with a 500 mA current limitation.

The LED LD6 indicates that the power switch (U1) is ON and STM3240G-EVAL functions as a USB host or that the VBUS is powered by another USB host while STM3240G-EVAL functions as a USB device. The LED LD5 indicates an over-current.

2.16 Ethernet

STM3240G-EVAL evaluation board enables 10/100M ethernet communication by a PHY DP83848CVV (U5) and integrated RJ45 connector (CN7). Both MII and RMII interface modes can be selected by setting jumpers JP5, JP6 and JP8 as listed below:

Table 11. Ethernet related jumpers and solder bridges

Jumper	Description
JP8	JP8 is used to select MII or RMII interface mode. To enable MII, JP8 is not fitted. To enable RMII interface mode, JP8 is fitted. Default setting: Not fitted
JP6	To enable MII interface mode, set JP6 as shown (Default setting):
	To enable RMII interface mode, set JP6 as shown:
JP5	To provide 25 MHz clock for MII or 50 MHz clock for RMII by MCO at PA8, set JP5 as shown (Default setting):
	To provide 25 MHz clock by external crystal X1 (for MII interface mode only) set JP5 as shown:
	When clock is provided by external oscillator U3, JP5 must not be fitted.
SB1	SB1 is used to select clock source only for RMII mode. To connect the clock from oscillator U3 to RMII_REF_CLK, close SB1. The resistor R212 has to be removed in this case. Default setting: Closed.

Note: 1 A test point (TP2) is available on the board for the PTP_PPS feature test.

2 The Ethernet PHY (U5) can be powered down by regulating PB14.

3 In RMII mode it is not possible to use MCO to output the 50 MHz clock to PHY due to the PLL limitation explained in chapter 2.6.5 of STM32F20x & STM32F21x Errata sheet (ES0005). In such a case it is possible to provide the 50 MHz clock by soldering a 50 MHz oscillator (ref SM7745HEV-50.0M or equivalent) on the U3 footprint located under CN3 and also removing jumper on JP5. This oscillator is not provided with the board.

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2.17 USB OTG HS

The STM3240G-EVAL evaluation board enables USB OTG high speed communication via a USB Micro-AB connector (CN9), USB high speed PHY (U8) and USB power switch (U4) connected to VBUS. The evaluation board can be powered by this USB connector (CN9) at 5V DC with a 500 mA current limitation.

The LED LD7 indicates that power switch (U4) is ON and STM3240G-EVAL is working as a USB host or that VBUS is powered by another USB host when STM3240G-EVAL is working as a USB device. The LD8 indicates an over-current.

The USB ULPI bus is shared with CAN2 bus, the JP10 and JP3 must be kept open for USB OTG HS.

Table 12. MicroSD card related jumper

Jumper	Description
JP31	To disable USB OTG PHY U8, JP31 is not fitted. Default setting: Fitted

Note: On boards MB786 prior to version B03 it is possible that after a board RESET the MCU is no longer able to control communication with the OTG PHY (U8). When this issue occurs the only way to recover OTG PHY control is to power the board OFF and ON. This issue is fixed on MB786 version B03 or newer.

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2.18 Camera module

A camera module is connected to DCMI bus of STM32F407IGH6 and shares the same I/Os with the motor control connector. SB16 must be kept open for camera module application.

There are two possible modules and omnivision cameras populated on the CN15 connector of the board:

- 1.3 Megapixel: Module CN01302H1045-C: Camera OV9655
- 2 Megapixel: Module CN020VAH2554-C: Camera OV2640

Table 13. Camera module related jumpers

Jumper	Description
JP26	To set power down mode for the camera module, JP26 is fitted. Default setting: Not Fitted
SB16	Description of SB16 is in *Section 2.9: Motor control*.

Note: 1 When the camera demo loaded in Flash is executed, some green pixels may appear in high contrast zones, depending on the image captured.

2 The camera is not firmly restricted on its connector (CN15). It is possible that during shipment the camera could be unplugged. In such case you need to plug it into the right position as shown on the picture below (pin 1 dot on top left corner of the socket).

It is not recommended to remove it in order to avoid false contact later.

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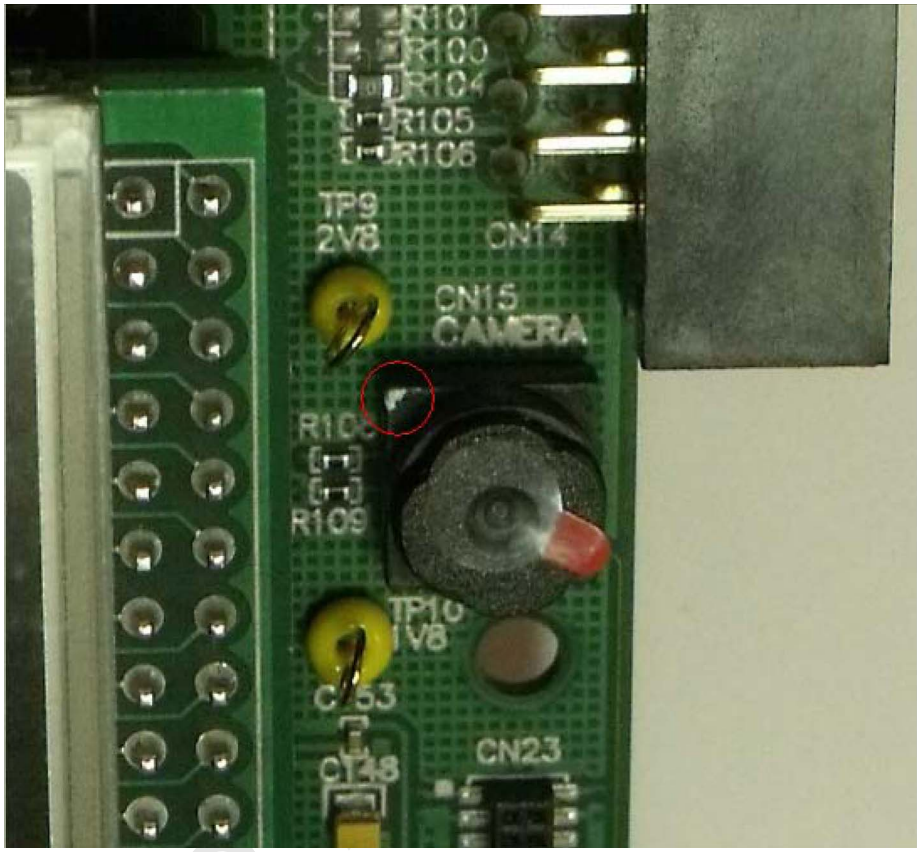
Figure 4. Pin 1 camera plug



The camera extension connector CN23 is available on the boards to connect the ST camera plug-in board.

2.19 SRAM

The 16 Mbit SRAM is connected to FSMC bus of STM32F407IGH6 which shares the same I/Os with CAN1 bus. JP3 and JP10 must not be fitted for SRAM and LCD application.

Table 14. SRAM related jumpers

Jumper	Description
JP1	Connect PE4 to SRAM as A20 by setting JP1 as shown (Default setting): 1 2 3
	Connect PE4 to trace connector CN13 as TRACE_D1 by setting JP1 as shown: 1 2 3
JP2	Connect PE3 to SRAM as A19 by settiing JP2 as shown (Default setting): 1 2 3
	Connect PE3 to trace connector CN13 as TRACE_D0 by setting JP2 as shown: 1 2 3

2.20 Development and debug support

Version 2 of the ST-LINK, called ST-LINK/V2, is embedded on the board. This tool allows onboard program loading and debugging of the STM32F using the JTAG or SWD interface. Third-party debug tools are also supported by the JTAG (CN14) or Trace (CN13) connectors.

To communicate with the embedded ST-LINK/V2, a specific driver needs to be installed on your PC. To download and install this driver, refer to the software and development tools page for the STM32F family available on www.st.com (the install shield is called ST-LINK_V2_USBdriver.exe).

Third-party toolchains, Atollic TrueSTUDIO, KEIL ARM-MDK, IAR EWARM and Tasking VX-Toolset support ST-LINK/V2 according to the following table:

Table 15. Third-party toolchain support

Manufacturer	Toolchain	Version
Atollic	TrueSTUDIO	2.1
IAR	EWARM	6.20.4
Keil	MDK-ARM	4.20
Tasking	VX-Toolset	ARM Cortex-M 4.0.1

The embedded ST-LINK/V2 connects to the PC via a standard USB cable from connector CN21. The bicolor LED LD10 (COM) indicates the status of the communication as follows:

- Slow blinking Red/Off: At power-on before USB initialization
- Fast blinking Red/Off: After the first correct communication between PC and ST-LINK/V2 (enumeration)
- Red LED On: When initialization between PC and ST-LINK/V2 is successfully finished
- Green LED On: After successful target communication initialization
- Blinking Red/Green: During communication with target
- Green On: Communication finished and OK
- Orange On: Communication failure

Note: It is possible to power the board via CN21 (embedded ST-LINK/V2 USB connector) even if an external tool is connected to CN13 (trace) or CN14 (external JTAG and SWD).

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2.21 Display and input devices

The 3.2” TFT color LCD connected to FSMC bus and 4 general purpose color LEDs (LD 1, 2, 3, 4) are available as display devices. A touchscreen connected to an I/O expander (U24), 4-direction joystick with selection key, general purpose button (B4), wakeup button (B2) and tamper detection button (B3) are available as input devices.

Table 16. LCD modules

Pin on CN19	Pin name	Pin connection	Pin on CN19	Pin name	Pin connection
1	CS	FSMC_NE3 (PG10)	18	PD14	FSMC_D12
2	RS	FSMC_A0	19	PD15	FSMC_D13
3	WR/SCL	FSMC_NWE	20	PD16	FSMC_D14
4	RD	FSMC_NOE	21	PD17	FSMC_D15
5	RESET	RESET#	22	BL_GND	GND
6	PD1	FSMC_D0	23	BL_Control	+5V
7	PD2	FSMC_D1	24	VDD	+3V3
8	PD3	FSMC_D2	25	VCI	+3V3
9	PD4	FSMC_D3	26	GND	GND
10	PD5	FSMC_D4	27	GND	GND
11	PD6	FSMC_D5	28	BL_VDD	+5V
12	PD7	FSMC_D6	29	SDO	NC
13	PD8	FSMC_D7	30	SDI	NC
14	PD10	FSMC_D8	31	XL	I/O expander U24
15	PD11	FSMC_D9	32	XR	I/O expander U24
16	PD12	FSMC_D10	33	YD	I/O expander U24
17	PD13	FSMC_D11	34	YU	I/O expander U24

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3 Connectors

3.1 Daughter board extension connectors CN1, 2, 3 and 4

Four male headers, CN1, 2, 3 and 4, can be used to connect with a daughterboard or standard wrapping board to STM3240G-EVAL evaluation board. A total number of 140 GPIOs are available on the board.

Each pin on CN1, 2, 3 and 4 can be used by a daughterboard after disconnecting it from the corresponding function block on STM3240G-EVAL evaluation board. Please refer to *Table 17* and *Table 20* for details.

Table 17. Daughter board extension connector CN1

Pin	Description	Alternative function	How to disconnect with function block on STM3240G-EVAL board
1	GND	-	-
3	PE3	Trace_D0 and FSMC_A19	Keep JP2 on 2<->3
5	PE5	Trace_D2	
7	PI8	LCD_HSYNC	-
9	PC14	OSC32_IN	Remove R84, SB4 closed
11	PC15	OSC32_OUT	Remove R85, SB5 closed
13	PI10	MII_RX_ER	Remove RS3
15	PF0	FSMC_A0	-
17	PF2	FSMC_A2	-
19	GND	-	-
21	PF5	FSMC_A5	-
23	PF7	Smartcard_RST	-
25	PF9	Potentiometer	Remove R151
27	PH0	OSC_IN	SB6 closed
29	PC0	ULPI_STP	-
31	PC1	MII_MDC	SB11 open
33	PC3	MII_TX_CLK	Remove R51
35	PA0	WakeUP	Remove R139
37	PA2	MII_MDIO	-
39	GND	-	-
41	PH4	ULPI_NXT	Remove R61
43	NC	-	-
45	NC	-	-
47	EMU_3V3	-	-

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Table 17. Daughter board extension connector CN1 (continued)

Pin	Description	Alternative function	How to disconnect with function block on STM3240G-EVAL board
49	EMU_5V	-	-
2	PE2	Trace_CLK	-
4	PE4	Trace_D1 & FSMC_A20	Keep JP1 on 2<->3
6	PE6	Trace_D3	-
8	PC13	Anti-Tamper	Remove R143
10	GND	-	-
12	PI9	LED3	Remove R141
14	PI11	ULPI_DIR	Remove R62
16	PF1	FSMC_A1	-
18	PF3	FSMC_A3	-
20	PF4	FSMC_A4	-
22	PF6	Smartcard_OFF	Remove R126
24	PF8	LCD_CS	
26	PF10	Audio_IN	Remove R196
28	PH1	OSC_OUT	Remove R86, SB7 closed
30	GND	-	-
32	PC2	MII_TXD2 & MC	SB12 open
34	VREF+	-	-
36	PA1	MII_RX_CLK	JP6 open
38	PH2	MII_CRS	Remove RS3
40	PH3	MII_COL	Remove RS3
42	PH5	OTG_FS_PowerSwitchOn	Remove R18
44	NC	-	-
46	NC	-	-
48	APP_3V3	-	-
50	GND	-	-

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Table 18. Daughterboard extension connector CN2

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
1	GND	-	-
3	PA3	ULPI_D0	-
5	PA5	ULPI_CLK	Remove R69
7	PA7	MII_RX_DV	Remove RS2, JP8 open
9	PC5	MII_RXD1	Remove R58
11	PB0	ULPI_D1	-
13	PB2	BOOT1	-
15	PF12	FSMC_A6	-
17	PF14	FSMC_A8	-
19	GND	-	-
21	PG1	FSMC_A11	-
23	PE8	FSMC_D5	-
25	PE10	FSMC_D7	-
27	PE12	FSMC_D9	-
29	PE14	FSMC_D11	-
31	PE15	FSMC_D12	-
33	PB11	ULPI_D4	-
35	PH7	MII_RXD3	Remove RS3
37	PH9	DCMI_D0	Remove camera module from CN15
39	GND	-	-
2	APP_VCC	-	-
4	PA4	Audio_DAC_OUT	Remove R115
6	PA6	DCMI_PIXCK	Remove camera module from CN15
8	PC4	MII_RXD0	Remove RS2
10	GND	-	-
12	PB1	ULPI_D2	-
14	PF11	OTG_FS_Overcurrent	Remove R15
16	PF13	FSMC_A7	-
18	PF15	FSMC_A9	-
20	PG0	FSMC_A10	-
22	PE7	FSMC_D4	-
24	PE9	FSMC_D6	-
26	PE11	FSMC_D8	-

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Table 18. Daughterboard extension connector CN2 (continued)

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
28	PE13	FSMC_D10	-
30	GND	-	-
32	PB10	ULPI_D3	-
34	PH6	MII_RXD2	Remove RS5
36	PH8	DCMI_HSYNC & MC	Remove camera module from CN15. Disconnect motor control board from CN5.
38	PH10	DCMI_D1 &MC	Remove camera module from CN15. Disconnect motor control board from CN5.
40	PH11	DCMI_D2 &MC	Remove camera module from CN15. Disconnect motor control board from CN5.

Table 19. Daughter board extension connector CN3

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
1	GND	-	-
3	PI1	I2S_CK	-
5	PH15	Smartcard_3/5V and MC	Disconnect motor control board from CN5
7	PH13	MicroSDCard_detect and MC	Remove MicroSD card from CN6. Disconnect motor control board from CN5.
9	PC13	Anti-tamper	Remove R143
11	RESET#	Reset button	-
13	PA11	OTG_FS_DM	Remove R17
15	PA9	VBUS_FS	Remove USB cable from CN8. Remove R18.
17	PC9	MicroSDCard_D1 & I2S_CKIN	Keep JP16 on open
19	EMU_5V	-	-
21	PC6	I2S_MCK & Smartcard_IO	JP21 open
23	PG7	Smartcard_CLK	-
25	PG5	FSMC_A15	-
27	PG3	FSMC_A13	-
29	PD15	FSMC_D1	-
31	PD14	FSMC_D0	-
33	PD12	FSMC_A17	SB15 open
35	PD10	FSMC_D15	-

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Table 19. Daughter board extension connector CN3 (continued)

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
37	PD8	FSMC_D13	-
39	GND	-	-
41	PB13	ULPI_D6 & CAN2_TX	-
43	PH12	DCMI_D3 & MC	Remove camera module from CN15. Disconnect motor control board from CN5.
45	NC	-	-
47	EMU_3V3	-	-
49	EMU_5V	-	-
2	PI2	IO_Expandor_INT	Remove R136
4	PI0	I2S_CMD	-
6	PH14	DCMI_D4 & MC	Remove camera module from CN15. Disconnect motor control board from CN5.
8	PA13	TMS/SWDIO	-
10	GND	-	-
12	PA12	OTG_FS_DP	Remove R19
14	PA10	OTG_FS_ID	Remove R21
16	PA8	MCO	JP5 open
18	PC8	MicroSDCard_D0 & MC	Remove MicroSD card from CN6. Disconnect motor control board from CN5.
20	PC7	LED4	Remove R140
22	PG8	LED2	Remove R154
24	PG6	LED1	Remove R155
26	PG4	FSMC_A14	-
28	PG2	FSMC_A12	-
30	GND	-	-
32	PD13	FSMC/MC	SB14 open. Disconnect motor control board from CN5.
34	PD11	FSMC_A16	-
36	PD9	FSMC_D14	-
38	PB15	OneNAND_INT	Remove R53
40	PB14	MII_INT	Remove R41
42	PB12	ULPI_D5	-
44	NC	-	-
46	NC	-	-

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Table 19. Daughter board extension connector CN3 (continued)

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
48	APP_3V3	-	-
50	GND	-	-

Table 20. Daughter board extension connector CN4

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
1	GND	-	-
3	PI6	DCMI_D6 & MC	Remove camera module from CN15. Disconnect motor control board from CN5.
5	PI4	DCMI_D5 & MC	Remove camera module from CN15. SB16 open
7	PE0	FSMC_BL0	-
9	PB8	MII_TXD3 & MC	Remove RS5 SB10 open Disconnect motor control board from CN5.
11	BOOT0	BOOT0	-
13	PB6	I2C1_SCL	Remove R103
15	PB4	TRST	-
17	PG15	User button	Remove R150
19	GND	-	-
21	PG12	Smartcard_CMDVCC & LCD_VSYNC	Remove R128
23	PG10	FSMC_NE3	Remove LCD board MB785 from CN19
25	PD7	FSMC_NE1	Remove R52
27	PD5	FSMC_NWE	-
29	PD3	FSMC_CLK	-
31	PD2	MicroSDCard_CMD	-
33	PD0	FSMC_D2 & CAN1_RX	JP10 open
35	PC11	MicroSDCard_D3 & RS232/IrDA_RX	JP22 open Remove MicroSD card from CN6
37	PA15	TDI	-
39	GND	-	-
2	PI7	DCMI_D7 & MC	Remove camera module from CN15 Disconnect motor control board from CN5
4	PI5	DCMI_VSYNC & MC	Remove camera module from CN15 Disconnect motor control board from CN5

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Table 20. Daughter board extension connector CN4

Pin	Description	Alternative Function	How to disconnect with function block on STM3240G-EVAL board
6	PE1	FSMC_BL1	-
8	PB9	I2C1_SDA	Remove R111
10	GND	-	-
12	PB7	FSMC_NL	-
14	PB5	ULPI_D7 & CAN2_RX	JP10 open
16	PB3	TDO/SWO	-
18	PG14	MII_TXD1	Remove RS6
20	PG13	MII_TXD0	Remove RS6
22	PG11	MII_TX_EN	Remove RS6
24	PG9	FSMC_NE2	Remove R47
26	PD6	FSMC_NWAIT	Remove R54
28	PD4	FSMC_NOE	-
30	GND	-	-
32	PD1	FSMC_D3 & CAN1_TX	JP3 open
34	PC12	MicroSDCard_CLK	Remove MicroSD card from CN6
36	PC10	MicroSDCard_D2 & RS232/IrDA_TX	Remove MicroSD card from CN6
38	PA14	TCK/SWCLK	-
40	PI3	I2S_DIN	-

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3.2 Motor control connector CN5

Figure 5. Motor Control connector CN5

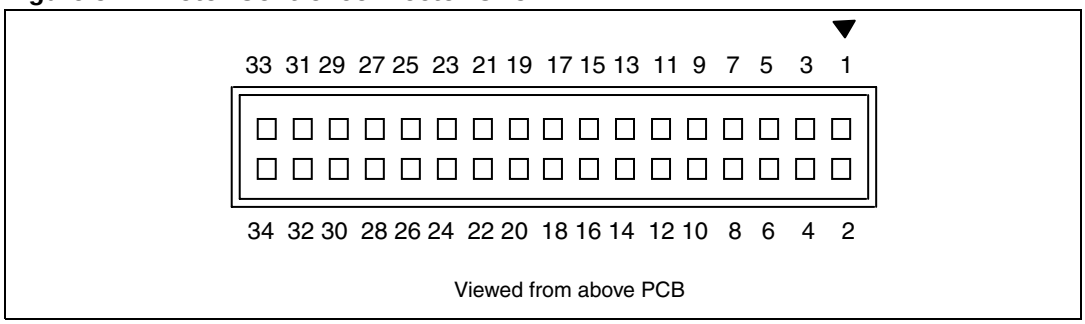

Table 21. Motor Control connector CN5

Description	STM32F407IGH6 pin	Pin number of CN5	Pin number of CN5	STM32F407IGH6 pin	Description
EMERGENCY STOP	PI4	1	2		GND
PWM-UH	PI5	3	4		GND
PWM-UL	PH13	5	6		GND
PWM-VH	PI6	7	8		GND
PWM-VL	PH14	9	10		GND
PWM-WH	PI7	11	12		GND
PWM-WL	PH15	13	14	PC4	BUS VOLTAGE
PHASE A CURRENT	PC1	15	16		GND
PHASE B CURRENT	PC2	17	18		GND
PHASE C CURRENT	PC3	19	20		GND
NTC BYPASS RELAY	PH8	21	22		GND
DISSIPATIVE BRAKE PWM	PC8	23	24		GND
+5V power	+5V	25	26	PC5	Heatsink temperature
PFC SYNC	PH10 and PH11	27	28		VDD_Micro
PFC PWM	PH12	29	30		GND
Encoder A	PD12	31	32		GND
Encoder B	PD13	33	34	PB8	Encoder Index

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3.3 MicroSD connector CN6

Figure 6. MicroSD connector CN6

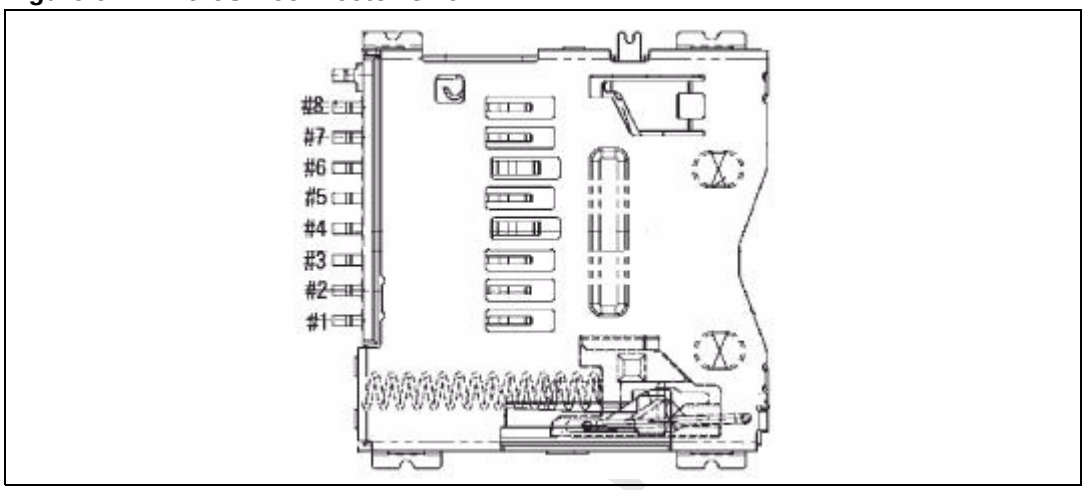

Table 22. MicroSD connector CN6

Pin number	Description	Pin number	Description
1	SDIO_D2 (PC10)	5	SDIO_CLK (PC12)
2	SDIO_D3 (PC11)	6	Vss/GND
3	SDIO_CMD (PD2)	7	SDIO_D0 (PC8)
4	+3V3	8	SDIO_D1 (PC9)
		10	MicroSDcard_detect (PH13)

3.4 Ethernet RJ45 connector CN7

Figure 7. Ethernet RJ45 connector CN7

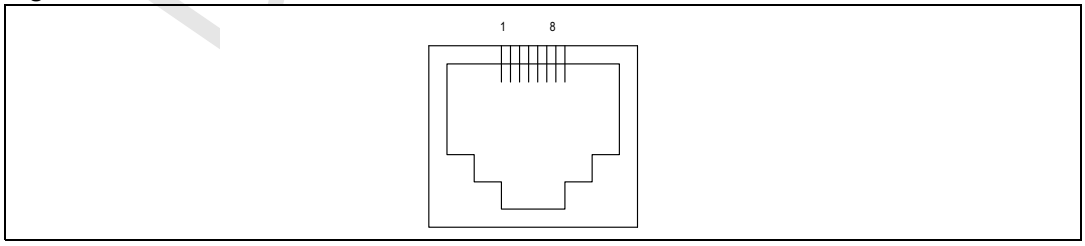

Table 23. RJ45 connector CN7

Pin number	Description	Pin number	Description
1	TxData+	2	TxData-
3	RxData+	4	Shield
5	Shield	6	RxData-
7	Shield	8	Shield

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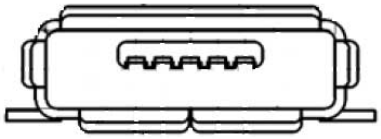
3.5 USB OTG FS Micro-AB connector CN8

Figure 8. USB OTG FS Micro-AB connector CN8



Front view

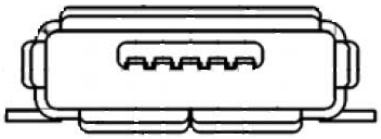
MS19172V1

Table 24. USB OTG FS Micro-AB connector CN8

Pin number	Description	Pin number	Description
1	VBUS (PA9)	4	ID (PA10)
2	D- (PA11)	5	GND
3	D+ (PA12)		

3.6 USB OTG HS Micro-AB connector CN9

Figure 9. USB OTG HS Micro-AB connector CN9



Front view

MS19172V1

Table 25. USB OTG HS Micro-AB connector CN9

Pin number	Description	Pin number	Description
1	VBUS	4	ID
2	D-	5	GND
3	D+		

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3.7 CAN D-type 9-pin male connectors CN10 (CAN1 or CAN2)

Figure 10. CAN D-type 9-pin male connector CN10 (CAN1 or CAN2)

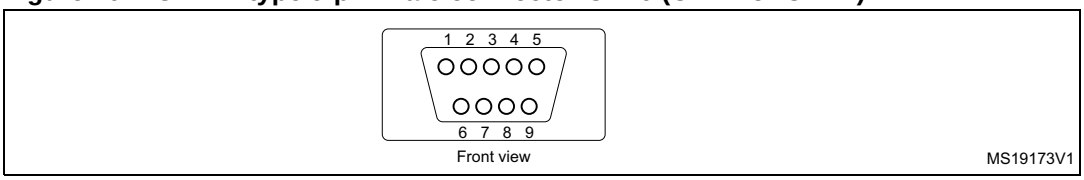

Table 26. CAN D-type 9-pin male connector CN10 (CAN1 or CAN2)

Pin number	Description	Pin number	Description
1,4,8,9	NC	7	CANH
3,5,6	GND	2	CANL

3.8 Audio connector CN11

A 3.5mm Stereo audio jack CN11 is available on STM3240G-EVAL board to support headset (headphone & microphone integrated).

3.9 Trace debugging connector CN13

Figure 11. Trace debugging connector CN13

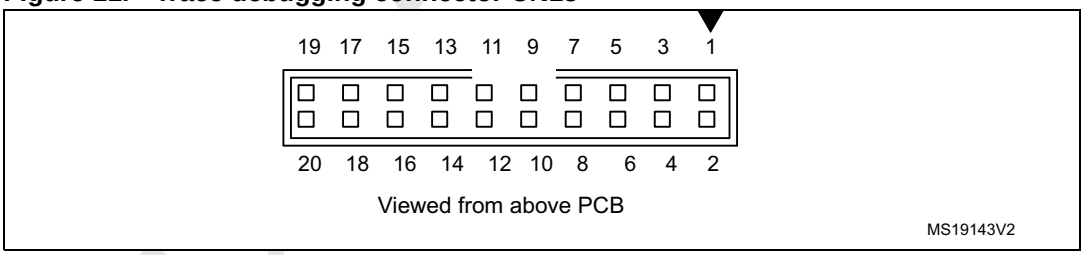

Table 27. Trace debugging connector CN13

Pin number	Description	Pin number	Description
1	3.3V power	2	TMS/PA13
3	GND	4	TCK/PA14
5	GND	6	TDO/PB3
7	KEY	8	TDI/PA15
9	GND	10	RESET#
11	GND	12	TraceCLK/PE2
13	GND	14	TraceD0/PE3 or SWO/PB3
15	GND	16	TraceD1/PE4 or nTRST/PB4
17	GND	18	TraceD2/PE5
19	GND	20	TraceD3/PE6

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3.10 JTAG debugging connector CN14

Figure 12. JTAG debugging connector CN14

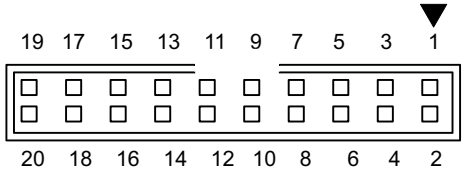

Table 28. JTAG debugging connector CN14

Pin number	Description	Pin number	Description
1	3.3V power	2	3.3V power
3	PB4	4	GND
5	PA15	6	GND
7	PA13	8	GND
9	PA14	10	GND
11	RTCK	12	GND
13	PB3	14	GND
15	RESET#	16	GND
17	DBGRQ	18	GND
19	DBGACK	20	GND

3.11 Camera module connector CN15

Figure 13. Camera module connector CN15 (front view)

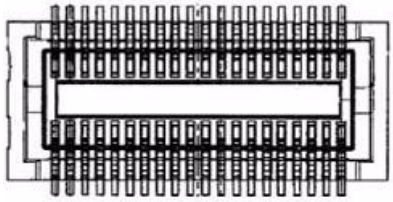

Table 29. Camera module connector CN15

Pin number	Description	Pin number	Description
1	DGND	13	XCLK1
2	DGND	14	Y6(PI6)
3	SIO_D (PB9)	15	DGND
4	AVDD (2.8V)	16	Y5(PI4)

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Table 29. Camera module connector CN15 (continued)

Pin number	Description	Pin number	Description
5	SIO_C (PB6)	17	PCLK (PA6)
6	RESET	18	Y4(PH14)
7	VSYNC (PI5)	19	Y0 (PH9)
8	PWDN	20	Y3(PH12)
9	HREF (PH8)	21	Y1(PH10)
10	DVDD (1.8V)	22	Y2(PH11)
11	DOVDD (2.8V)	23	AGND
12	Y7(PI7)	24	AGND

3.12 RS-232 connector CN16

Figure 14. RS-232 connector CN16 with ISP support (front view)

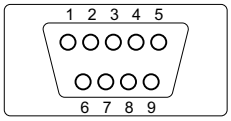


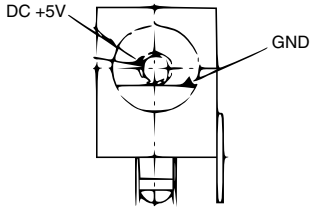
Table 30. RS-232 connector CN16 with ISP support

Pin number	Description	Pin number	Description
1	NC	6	Bootloader_BOOT0
2	RS232_RX (PC11)	7	NC
3	RS232_TX (PC10)	8	Bootloader_RESET
4	NC	9	NC
5	GND		

3.13 Power connector CN18

Your STM3240G-EVAL evaluation board can be powered from a 5V DC power supply via the external power supply jack (CN18) shown in *Figure 15*. The central pin of CN18 must be positive.

Figure 15. Power supply connector CN18



3.14 TFT LCD connector CN19

One 34-pin male header CN19 is available on the board for connecting LCD module board MB785. Please refer to *Section 2.21: Display and input devices* for details.

3.15 Smartcard connector CN20

Figure 16. Smartcard connector CN20

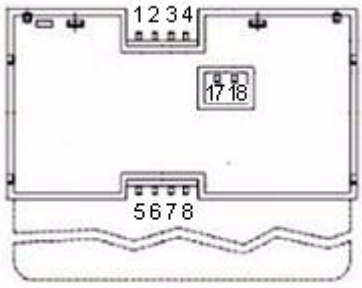


Table 31. Smartcard connector CN20

Pin number	Description	Pin number	Description
1	VCC	5	GND
2	RST	6	NC
3	CLK	7	I/O
4	NC	8	NC
17	Card presence detection pin	18	Card presence detection pin

3.16 ST-LINK/V2 connector CN21

The USB type B connector CN21 is for ST-LINK/V2 connected between the STM3240G-EVAL evaluation board and the PC for board debugging.

3.17 Camera extension connector CN23

Table 32. Camera extension connector CN23

Pin number	Description	Pin number	Description
1	+1V8	2	+1V8
3	GND	4	GND
5	NC	6	NC
7	GND	8	GND
9	NC	10	NC

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Table 32. Camera extension connector CN23

Pin number	Description	Pin number	Description
11	GND	12	GND
13	SCL	14	SDA
15	Camera_Plug	16	GND
17	Camera_RST	18	NC
19	Camera_XSDN	20	Camera_CLK
21	GND	22	GND
23	DCMI_D0	24	DCMI_D1
25	DCMI_D2	26	DCMI_D3
27	DCMI_D4	28	DCMI_D5
29	DCMI_D6	30	DCMI_D7
31	HSYSC	32	VSYSC
33	PIXCLK	34	NC
35	NC	36	NC
37	GND	38	GND
39	+2V8	40	+2V8

3.18 STM3240G-EVAL pinout

Table 33. STM3240G-EVAL pinout

Pin no.	Pin Name	Description
A2	PE2	TRACE_CLK / FSMC_A23
A1	PE3	TRACE_D0 / FSMC_A19
B1	PE4	TRACE_D1 / FSMC_A20
B2	PE5	TRACE_D2 / FSMC_A21
B3	PE6	TRACE_D3 / FSMC_A22
C1	VBAT	VBAT
D2	PI8- ANTI TAMP2	LCD_HSYNC
D1	PC13-ANTI_TAMP	ANTI-TAMPER_BUTTON
E1	PC14-OSC32_IN	32K_OSC
F1	PC15-OSC32_OUT	32K_OSC
D3	PI9	LED2
E3	PI10	ETHER_RX_ER
E4	PI11	USB_HS_DIR
F2	VSS_13	
F3	VDD_13	

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Table 33. STM3240G-EVAL pinout (continued)

Pin no.	Pin Name	Description
E2	PF0	FSMC_A0
H3	PF1	FSMC_A1
H2	PF2	FSMC_A2
J2	PF3	FSMC_A3
J3	PF4	FSMC_A4
K3	PF5	FSMC_A5
G2	VSS_5	
G3	VDD_5	
K2	PF6	SmartCard_OFF
K1	PF7	SmartCard_RESET
L3	PF8	LCD_CS
L2	PF9	POTENTIOMETER
L1	PF10	Audio_IN
G1	PH0 - OSC_IN	OSC_IN
H1	PH1 - OSC_OUT	OSC_OUT
J1	NRST	RESET_BUTTON
M2	PC0	USB_HS_STP
M3	PC1	ETHER_MDC / MC_ADC123_11 pin 15 (Current A)
M4	PC2	ETHER_TXD2 / MC_ADC123_1 2 pin 17 (Current B)
M5	PC3	ETHER_TX_CLK / MC_ADC123_13 pin 19 (Current C)
M1	VSSA	
N1	VREF-	
P1	VREF+	
R1	VDDA	
N3	PA0-WKUP	WAKEUP_BUTTON
N2	PA1	ETHER_RX_CLK
P2	PA2	ETHER_MDIO
F4	PH2	ETHER_CRS
G4	PH3	ETHER_COL
H4	PH4	USB_HS_NXT
J4	PH5	USB_FS_POWER_ON
R2	PA3	USB_HS_D0
L4	BYPASS	BYPASS
K4	VDD_4	
N4	PA4	Audio_DAC_OUT

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Table 33. STM3240G-EVAL pinout (continued)

Pin no.	Pin Name	Description
P4	PA5	USB_HS_CK
P3	PA6	CAM_PIXCK
R3	PA7	ETHER_DV
N5	PC4	ETHER_RXD0 / MC_ADC12_14 pin 14 (Bus voltage)
P5	PC5	ETHER_RXD1 / MC_ADC12_15 pin 26 (Heatsink temperature)
R5	PB0	USB_HS_D1
R4	PB1	USB_HS_D2
M6	PB2	BOOT1 /
R6	PF11	USB_FS_OVERCURRENT
P6	PF12	FSMC_A6
M8	VSS6	
N8	VDD_6	
N6	PF13	FSMC_A7
R7	PF14	FSMC_A8
P7	PF15	FSMC_A9
N7	PG0	FSMC_A10
M7	PG1	FSMC_A11
R8	PE7	FSMC_D4
P8	PE8	FSMC_D5
P9	PE9	FSMC_D6
M9	VSS_7	
N9	VDD_7	
R9	PE10	FSMC_D7
P10	PE11	FSMC_D8
R10	PE12	FSMC_D9
N11	PE13	FSMC_D10
P11	PE14	FSMC_D11
R11	PE15	FSMC_D12
R12	PB10	USB_HS_D3
R13	PB11	ULPI_D4
M10	VCAP1	VCAP / 1.2V
N10	VDD_1	
M11	PH6	ETHER_RXD2
N12	PH7	ETHER_RXD3

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Table 33. STM3240G-EVAL pinout (continued)

Pin no.	Pin Name	Description
M12	PH8	CAM_HSYNC / MC_NTC_bypass
M13	PH9	CAM_D0
L13	PH10	CAM_D1 / MC_TIM5_ETR pin 27 (PFC SYNC)
L12	PH11	CAM_D2 / MC_TIM3_CH2 pin 27 (PFCSYNC)
K12	PH12	CAM_D3 / TIM5_CH3 pin 29 (PFCPWM)
H12	VSS_14	
J12	VDD_14	
P12	PB12	ULPI_D5
P13	PB13	ULPI_D6 / CAN2_TX
R14	PB14	ETHER_INT
R15	PB15	OneNAND_INT
P15	PD8	FSMC_D13
P14	PD9	FSMC_D14
N15	PD10	FSMC_D15
N14	PD11	FSMC_A16
N13	PD12	FSMC_A17 / MC_TIM4_CH2 pin 33 (EnB)
M15	PD13	FSMC_A18 / MC_TIM4_CH2 pin 33 (EnB)
J13	VDD_8	
M14	PD14	FSMC_D0
L14	PD15	FSMC_D1
L15	PG2	FSMC_A12
K15	PG3	FSMC_A13
K14	PG4	FSMC_A14
K13	PG5	FSMC_A15
J15	PG6	LED0
J14	PG7	SmartCard_CK
H14	PG8	LED1
G12	VSS_9	
H13	VDD_9	
H15	PC6	SmartCard_IO / Audio_I2S_ MCK
G15	PC7	LED3
G14	PC8	SDIO_D0/ MC_TIM3_CH3 pin23 (Dissipative Brake)
F14	PC9	SDIO_D1 I2S_CKIN
F15	PA8	MCO
E15	PA9	USB_FS_VBUS

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Table 33. STM3240G-EVAL pinout (continued)

Pin no.	Pin Name	Description
D15	PA10	USB_FS_ID
C15	PA11	USB_FS_DM
B15	PA12	USB_FS_DP
A15	PA13	JTAG_TMS
F13	VCAP2	VCAP / 1.2V
F12	VSS 2	
G13	VDD_2	
E12	PH13	MC_TIM8_CH1N pin 5 (UL) / MicroSD card detect
E13	PH14	CAM_D4 / MC_TIM8_CH2N pin 9 (VL)
D13	PH15	MC_TIM8_CH3N pin 13 (WL) / SmartCard_3/5V
E14	PI0	Audio_I2S_WS
D14	PI1	Audio_I2S_CK
C14	PI2	Expander_INT
C13	PI3	Audio_I2S_DOUT
D9	VSS_15	
C9	VDD_15	
A14	PA14	JTAG_TCK
A13	PA15	JTAG_TDI
B14	PC10	SDIO_D2 / RS232_TX
B13	PC11	SDIO_D3 / RS232_RX
A12	PC12	SDIO_CK
B12	PD0	FSMC_D2 / CAN1_RX
C12	PD1	FSMC_D3 / CAN1_TX
D12	PD2	SDIO_CMD
D11	PD3	FSMC_CLK
D10	PD4	FSMC_NOE
C11	PD5	FSMC_NWE
D8	VSS_10	
C8	VDD_10	
B11	PD6	FSMC_NWAIT
A11	PD7	FSMC_NE1
C10	PG9	FSMC_NE2
B10	PG10	FSMC_NE3
B9	PG11	ETHER_TXEN
B8	PG12	SmartCard_CMDVCC

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Table 33. STM3240G-EVAL pinout (continued)

Pin no.	Pin Name	Description
A8	PG13	ETHER_TXD0
A7	PG14	ETHER_TXD1
D7	VSS_11	
C7	VDD_11	
B7	PG15	USER_BUTTON
A10	PB3	JTAG_TDO
A9	PB4	JTAG_TRST
A6	PB5	CAN2_RX / ETHER_PPS_OUT / ULPI_D7
B6	PB6	I2C1_SCL
B5	PB7	FSMC_NL
D6	BOOT0	BOOT0
A5	PB8	ETHER_TXD3 / MC_TIM4_CH3 pin 34 (Index)
B4	PB9	I2C1_SDA
A4	PE0	FSMC_NBL0
A3	PE1	FSMC_NBL1
D5	VSS_SA	
C6	VDD_3	POR Disable
C5	VDD_SA	
D4	PI4	CAM_D5 / MC_TIM8_BKIN pin 1 (Stop)
C4	PI5	CAM_VSYNC / MC_TIM8_CH1 pin 3 (UH)
C3	PI6	CAM_D6 / MC_TIM8_CH2 pin 7 (VH)
C2	PI7	CAM_D7 / MC_TIM8_CH3 pin 11 (WH)

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4 Schematics

The following schematic diagrams are listed:

- *Figure 17: STM3240G-EVAL on page 43*
- *Figure 18: MCU on page 44*
- *Figure 19: USB OTG HS on page 45*
- *Figure 20: Camera on page 46*
- *Figure 21: Ethernet on page 47*
- *Figure 22: Audio on page 48*
- *Figure 23: USB OTG FS on page 49*
- *Figure 24: SRAM on page 50*
- *Figure 25: LCD on page 51*
- *Figure 27: CAN on page 53*
- *Figure 28: I/O peripherals on page 54*
- *Figure 29: I/O expandor on page 55*
- *Figure 30: MicroSD card on page 56*
- *Figure 31: Motor control on page 57*
- *Figure 32: Smartcard on page 58*
- *Figure 33: JTAG and Trace on page 59*
- *Figure 34: Power on page 60*
- *Figure 35: Extension connector on page 61*
- *Figure 36: STLINK/V2 on page 62*
- *Figure 37: 3.2” LCD module with SPI and 16-bit interface on page 63*

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Figure 17. STM3240G-EVAL

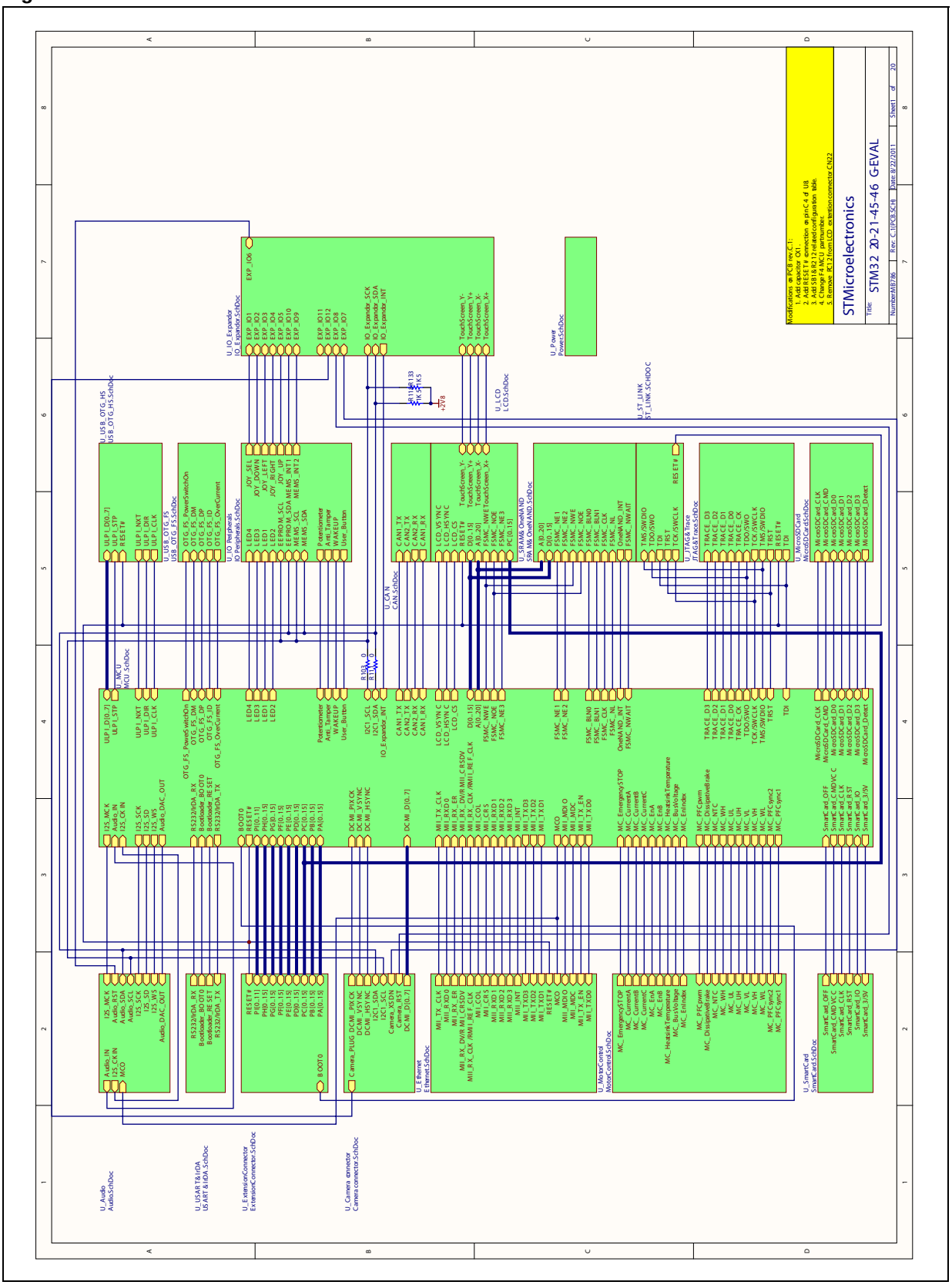


Figure 18. MCU

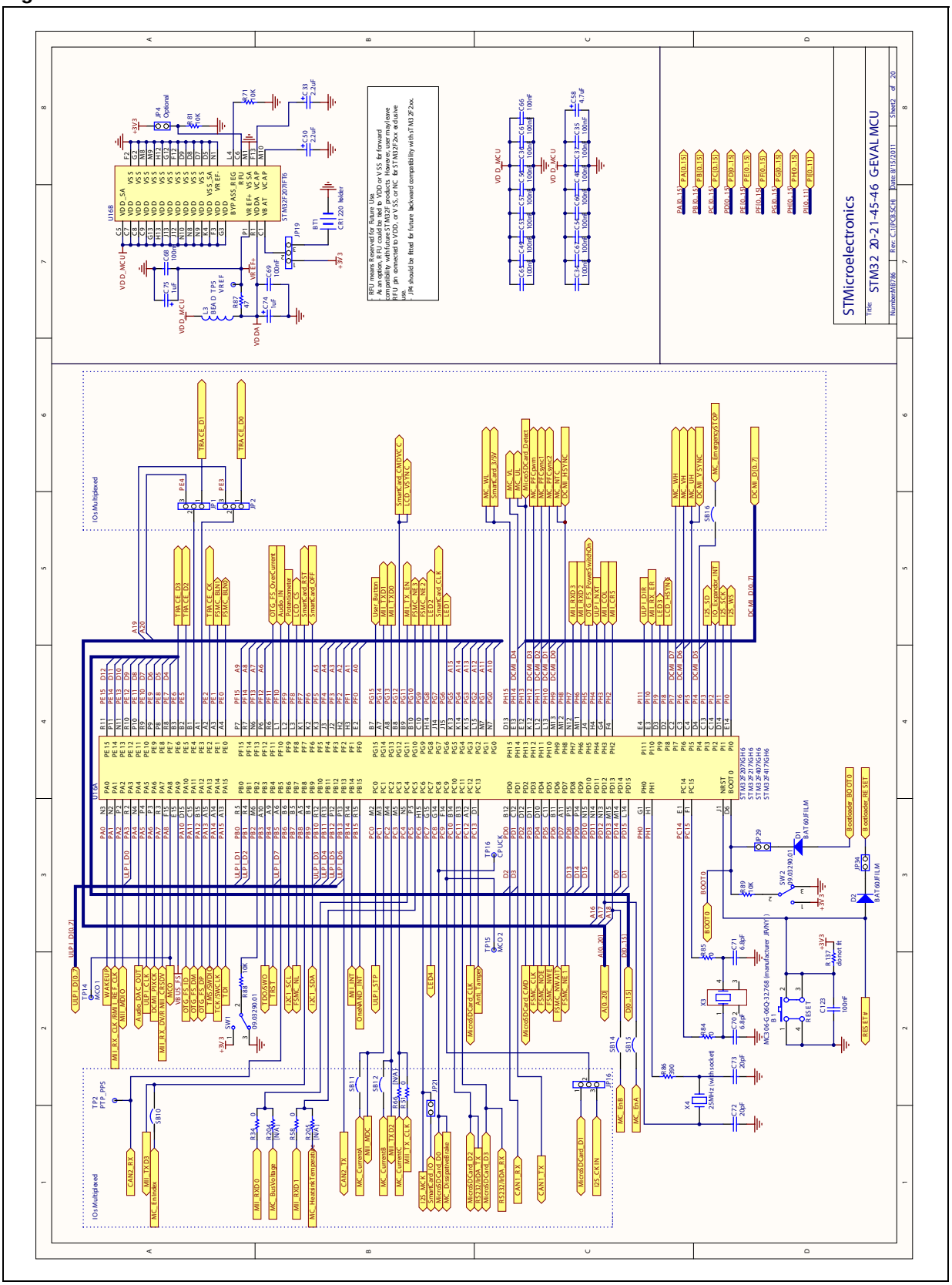


Figure 19. USB OTG HS

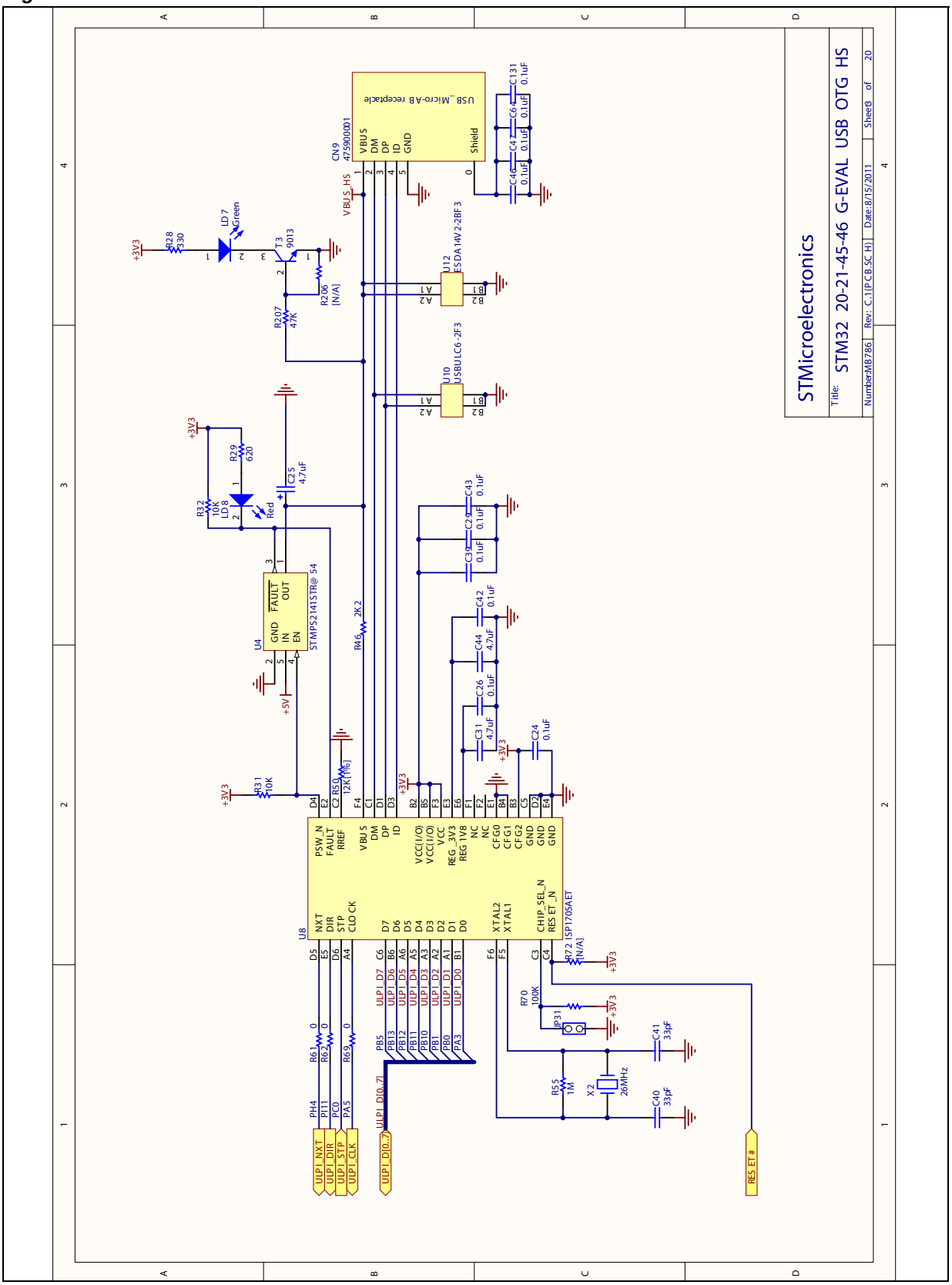


Figure 20. Camera

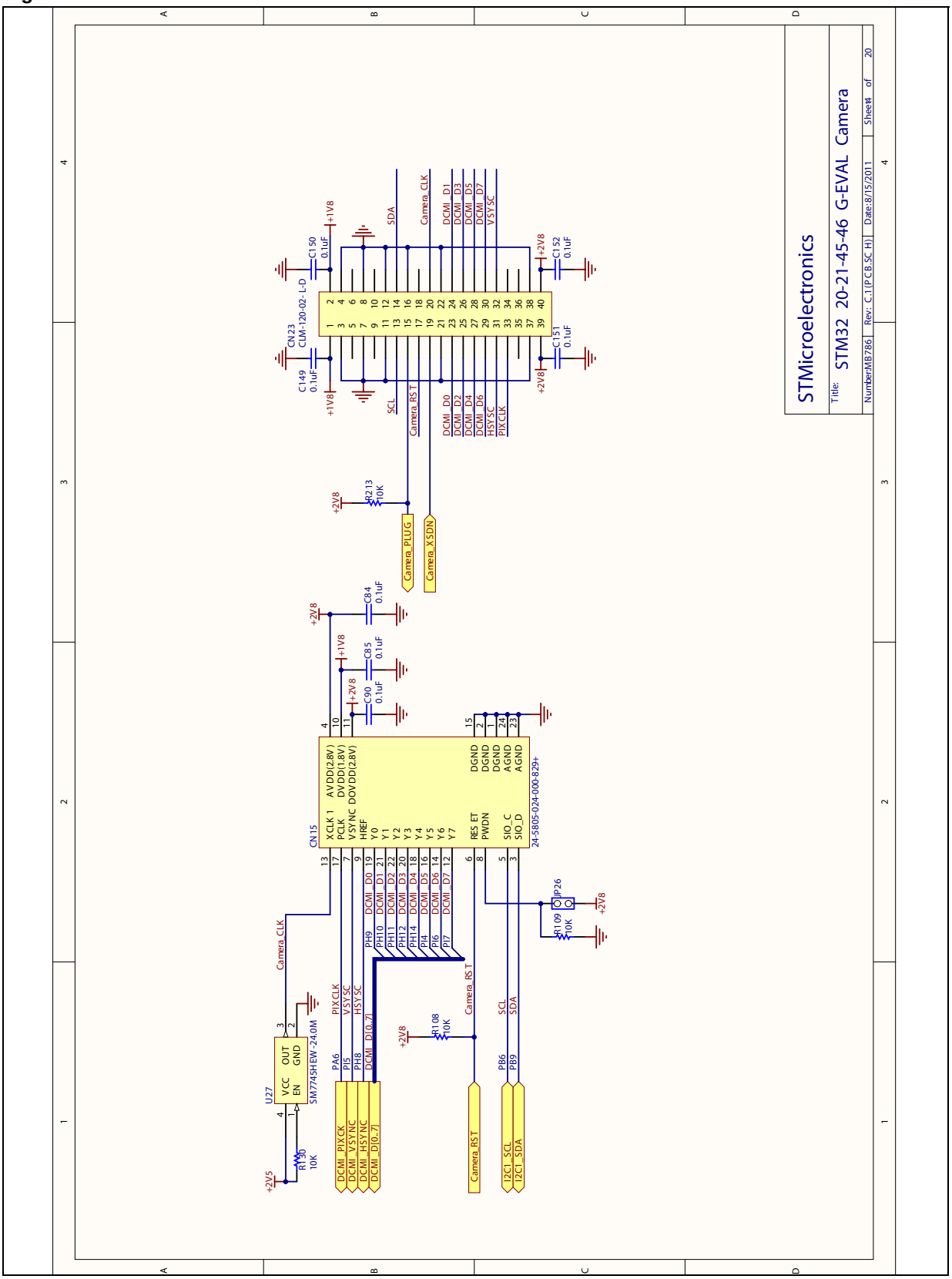


Figure 21. Ethernet

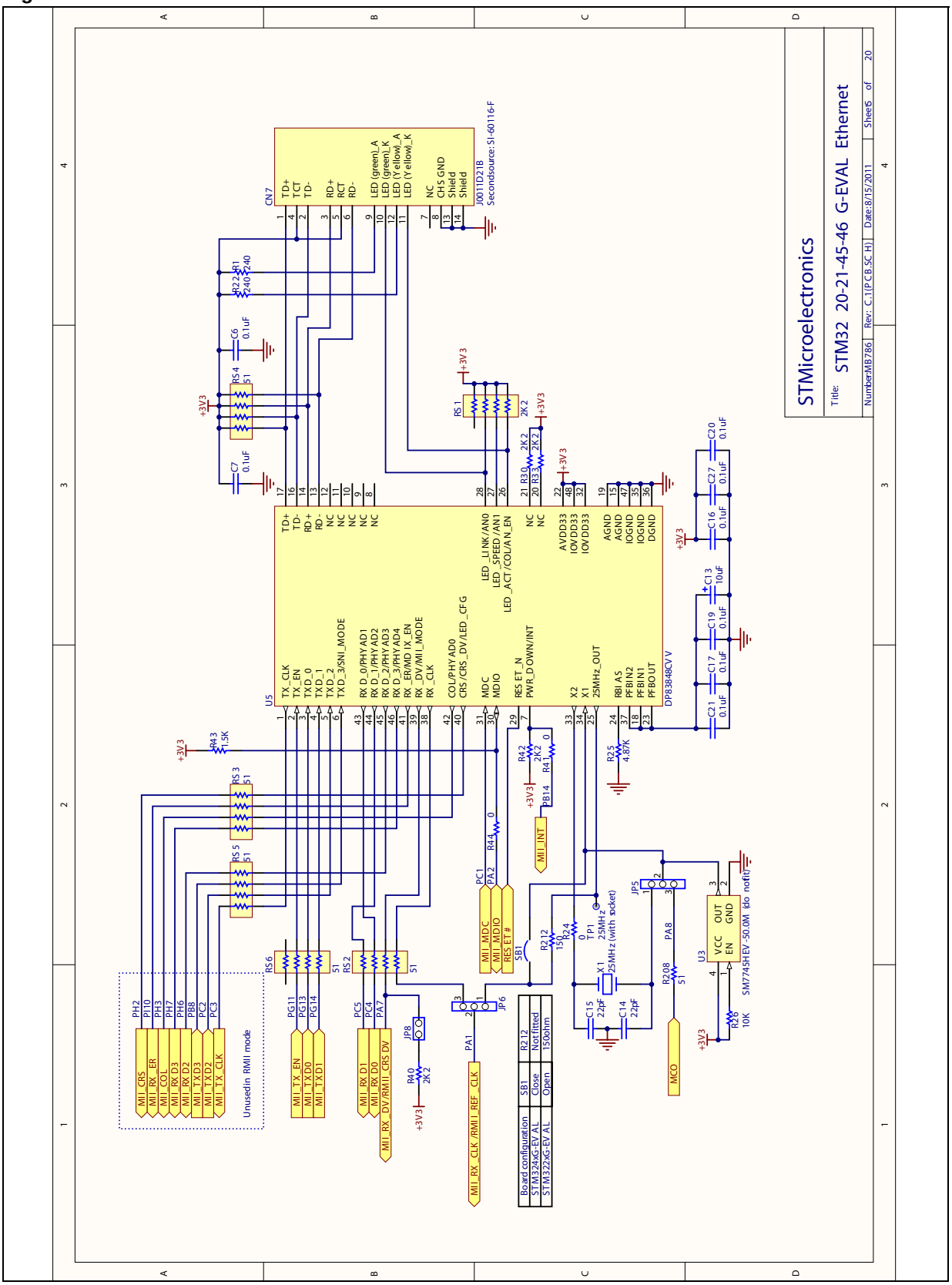


Figure 22. Audio

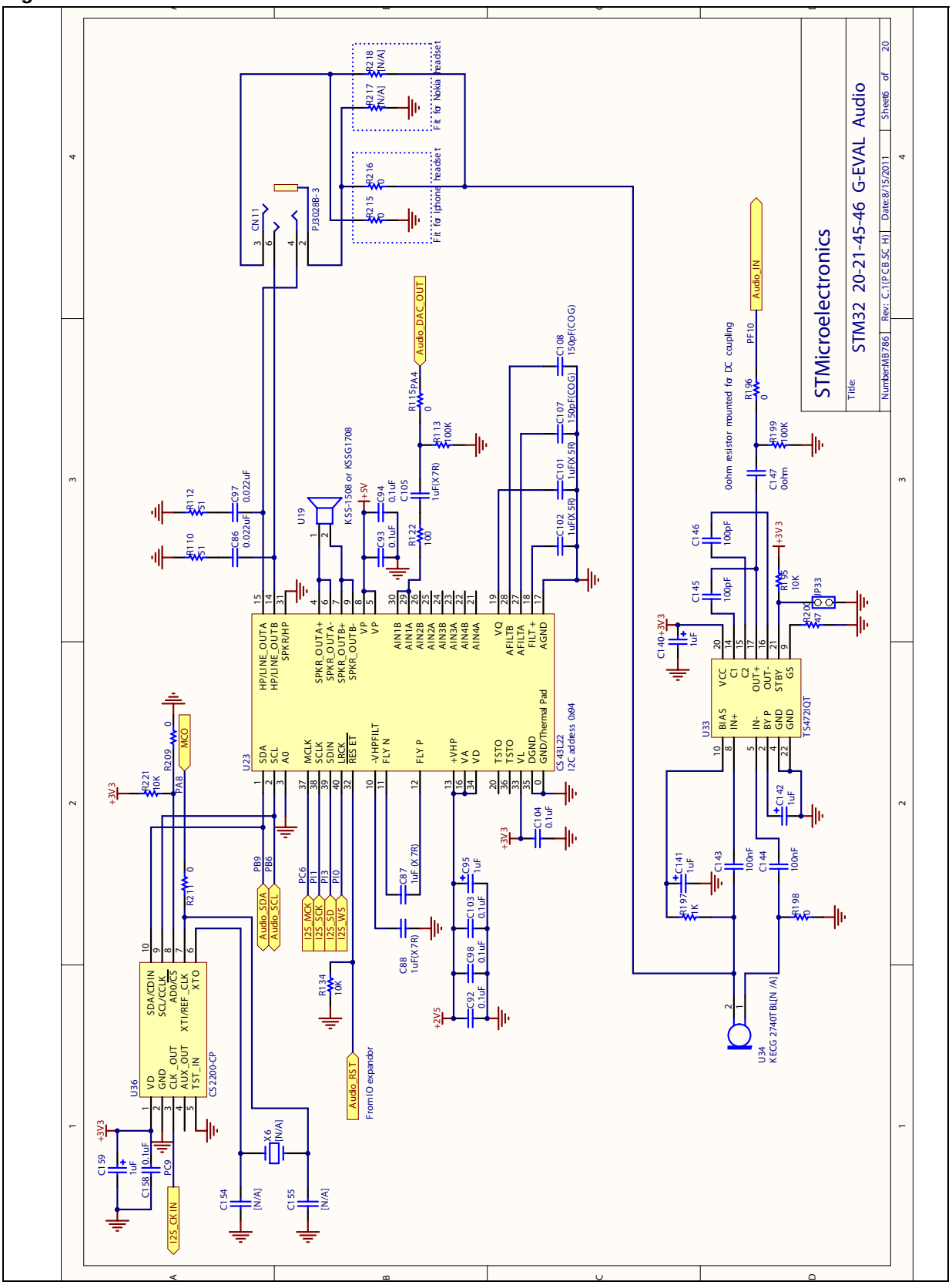


Figure 23. USB OTG FS

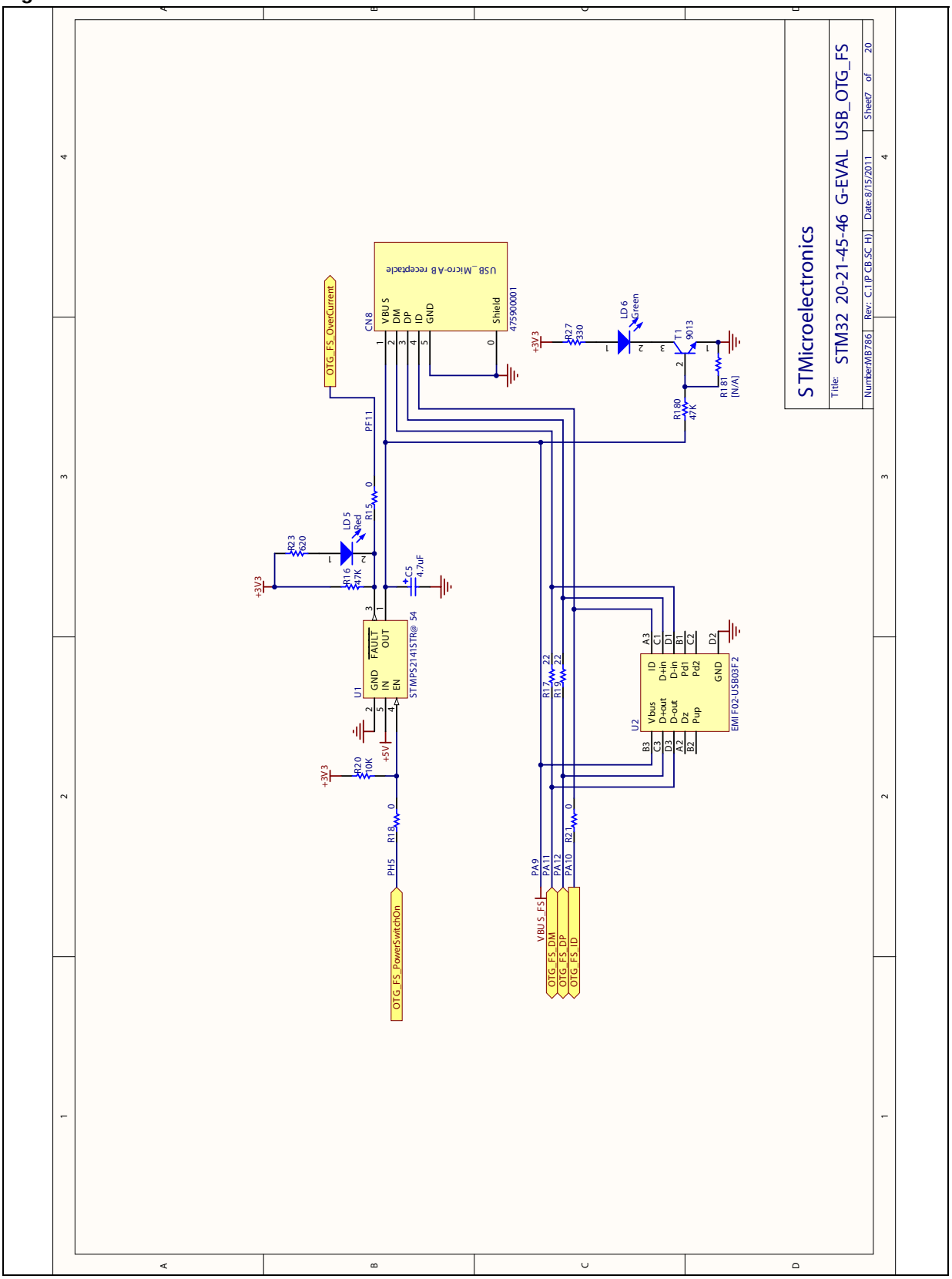


Figure 24. SRAM

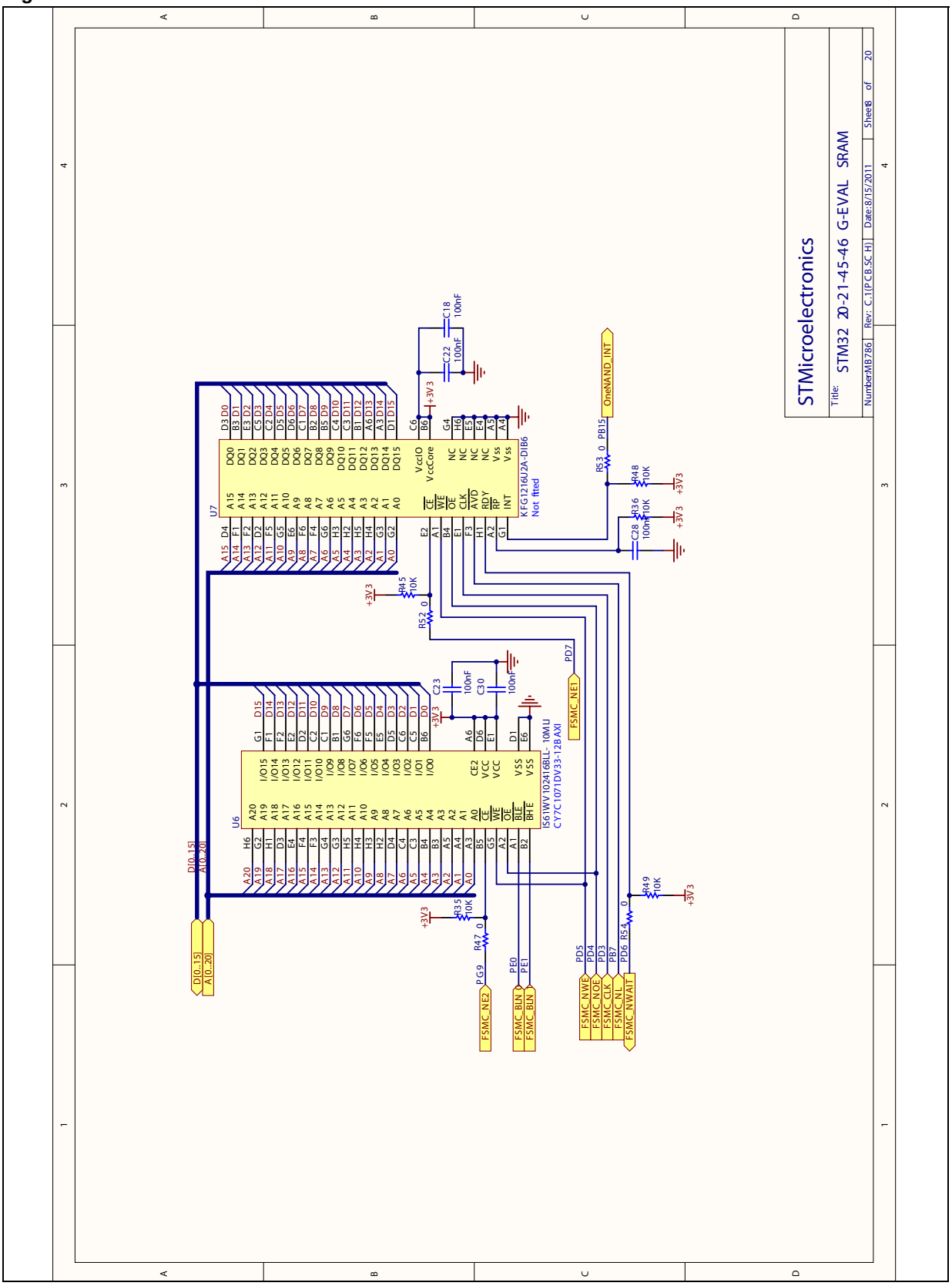


Figure 25. LCD

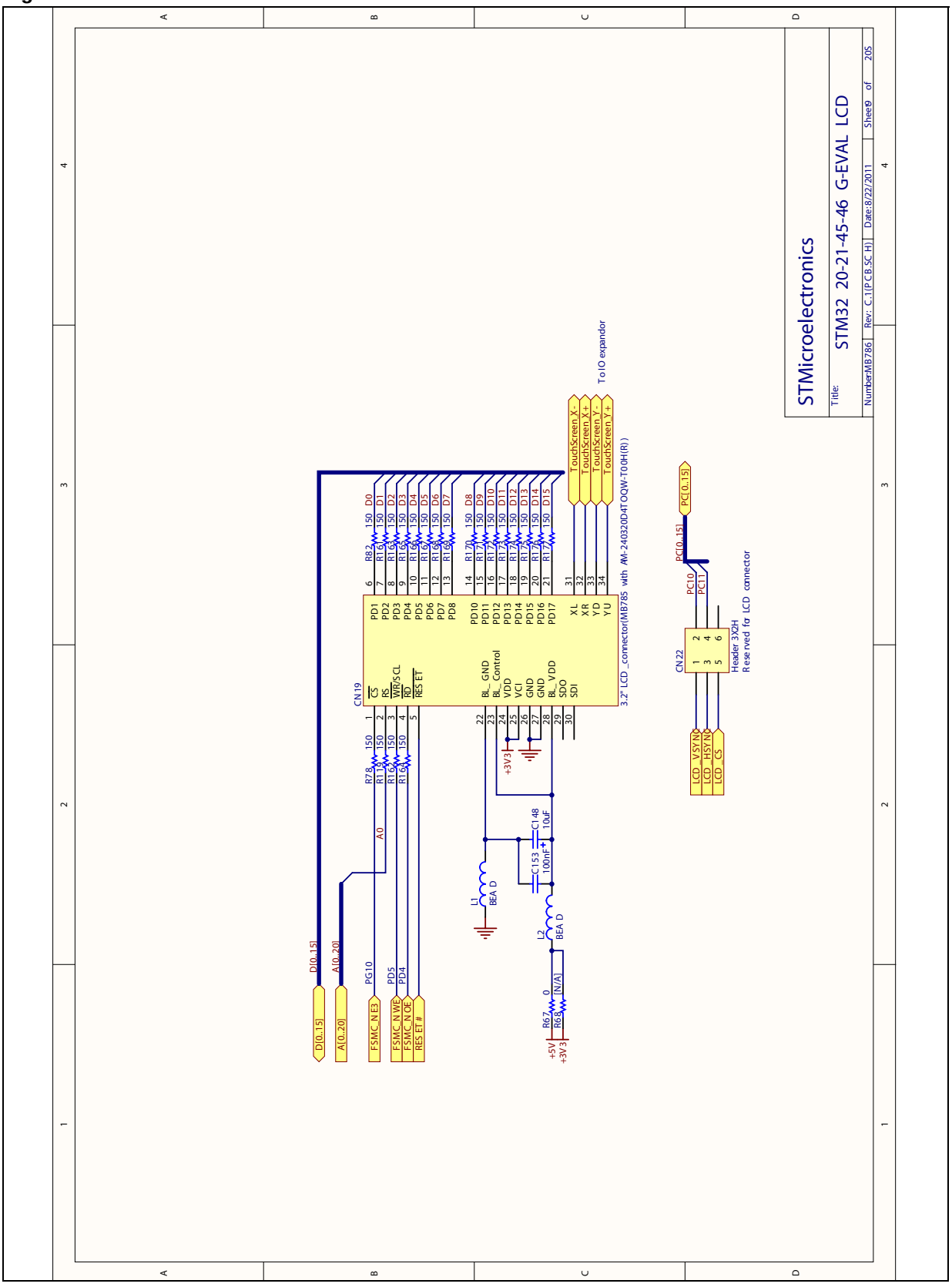


Figure 26. RS-232 and IrDA

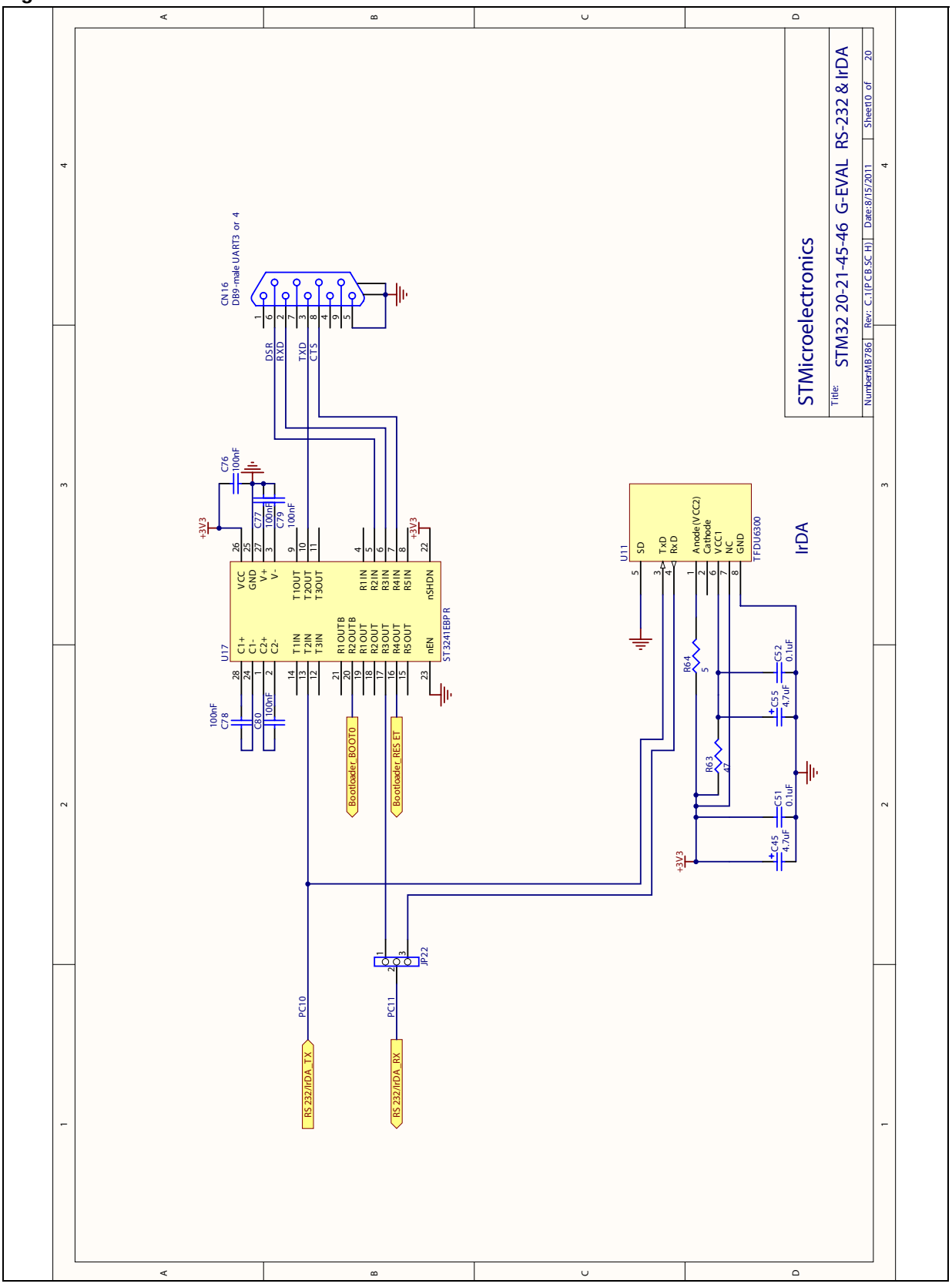


Figure 27. CAN

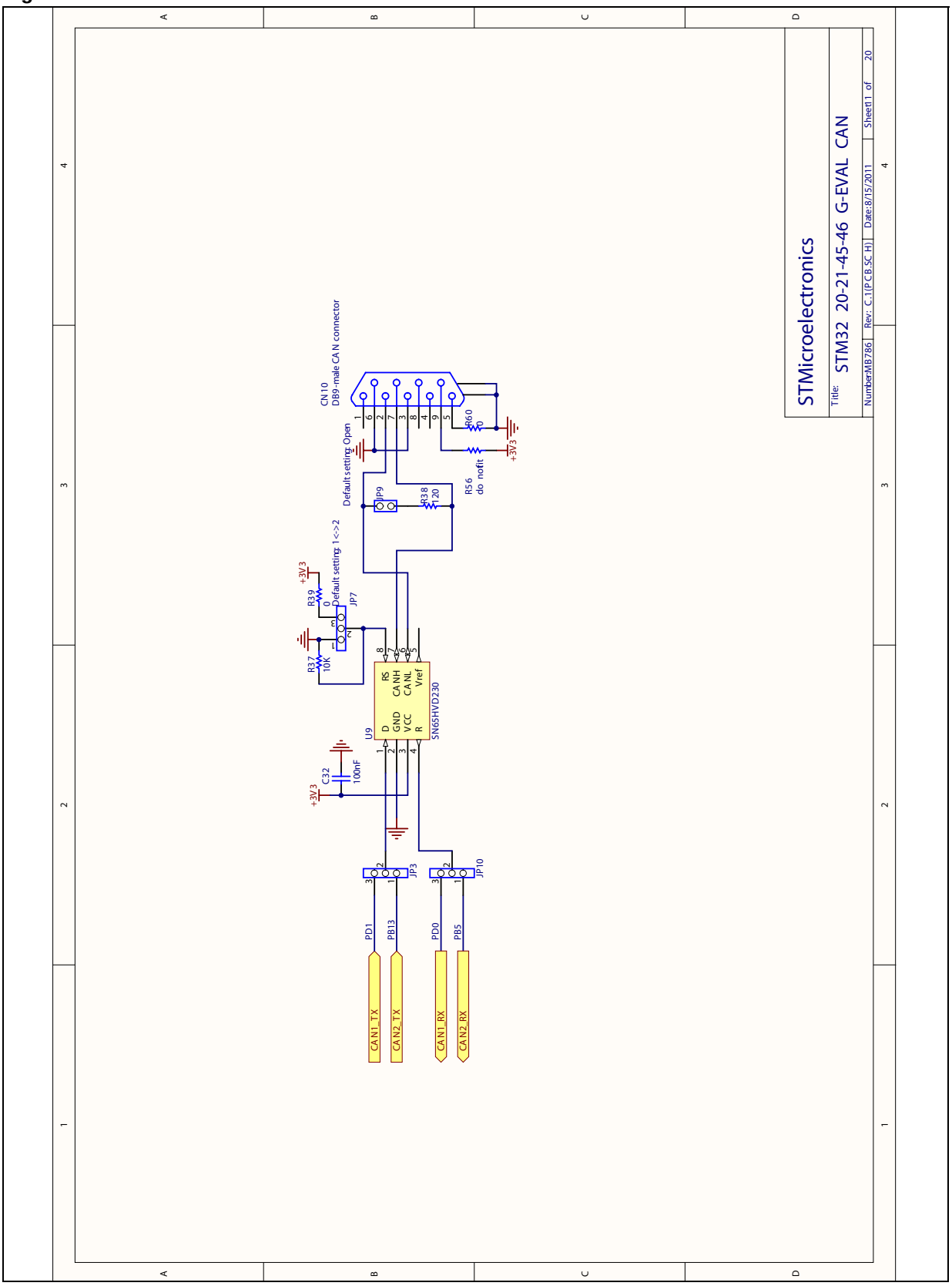


Figure 28. I/O peripherals

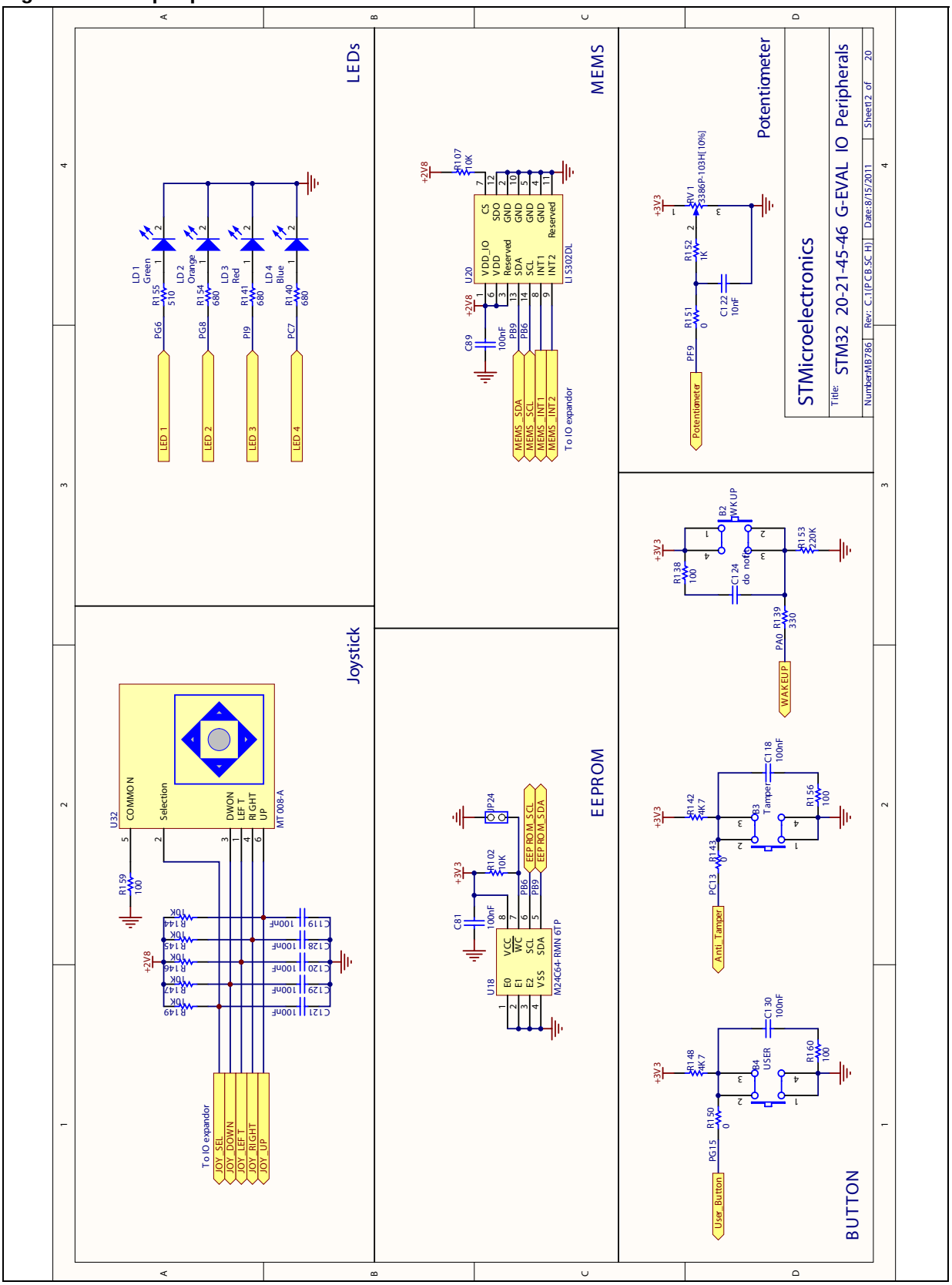


Figure 29. I/O expandor

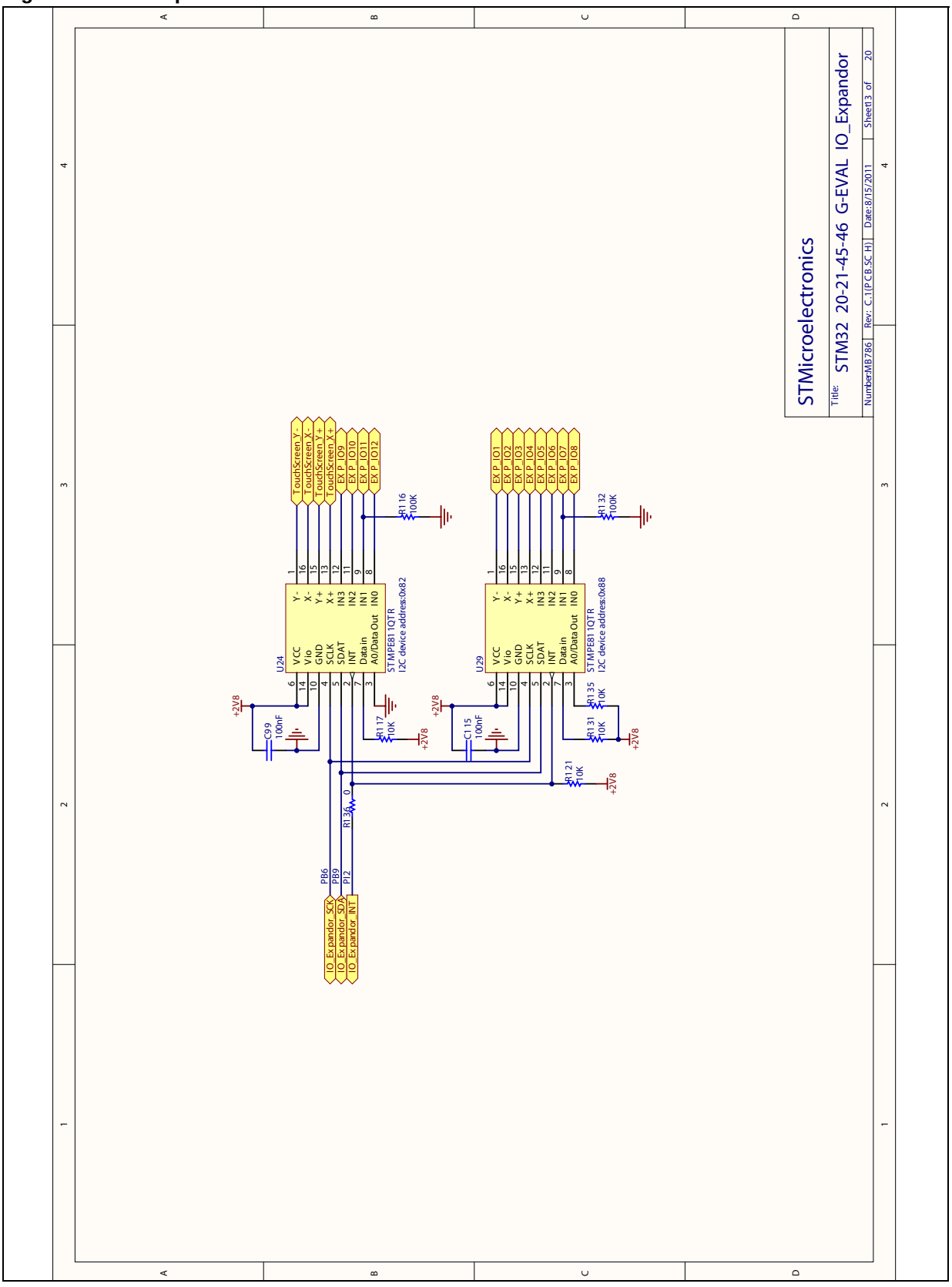


Figure 30. MicroSD card

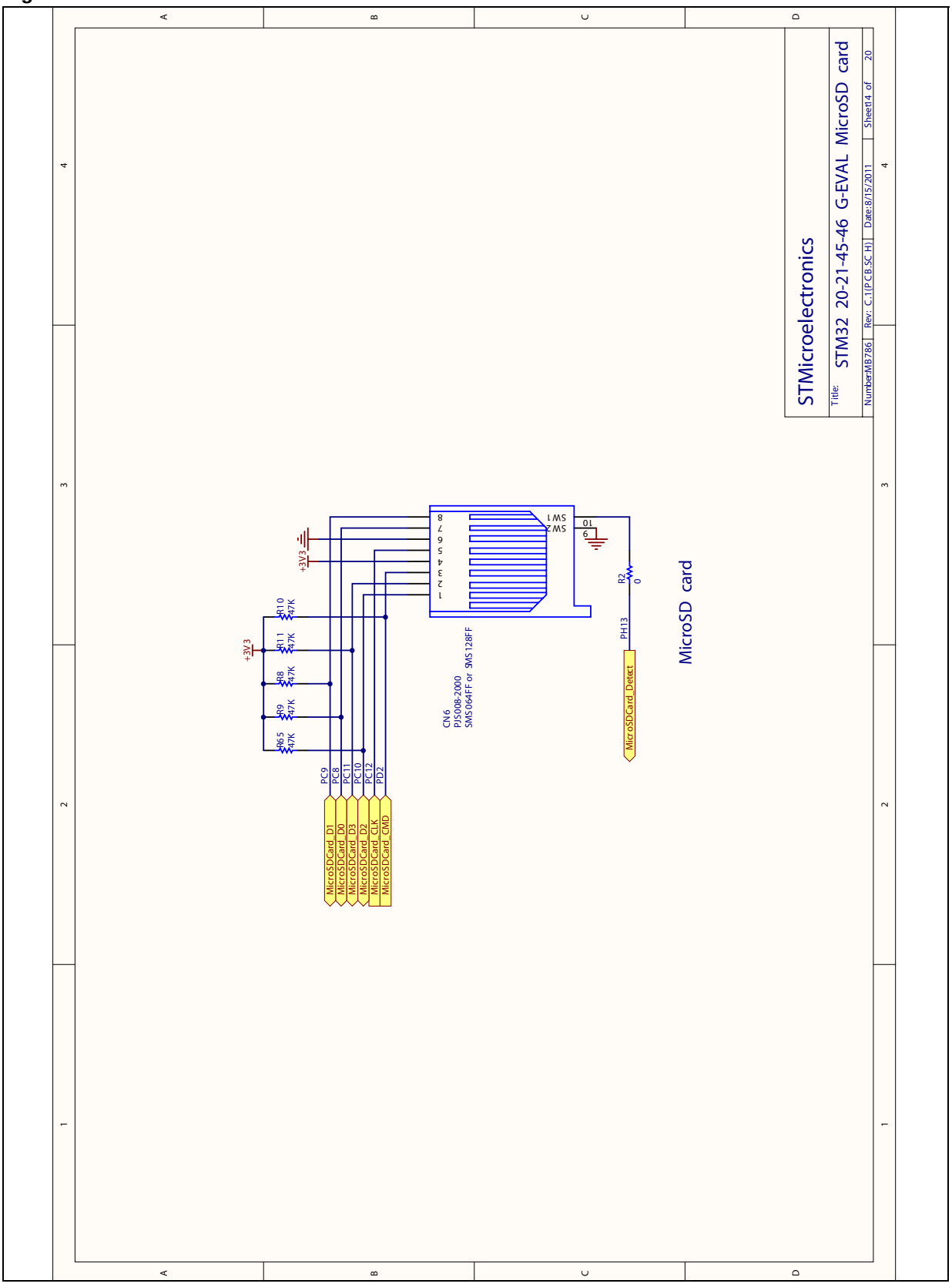


Figure 31. Motor control

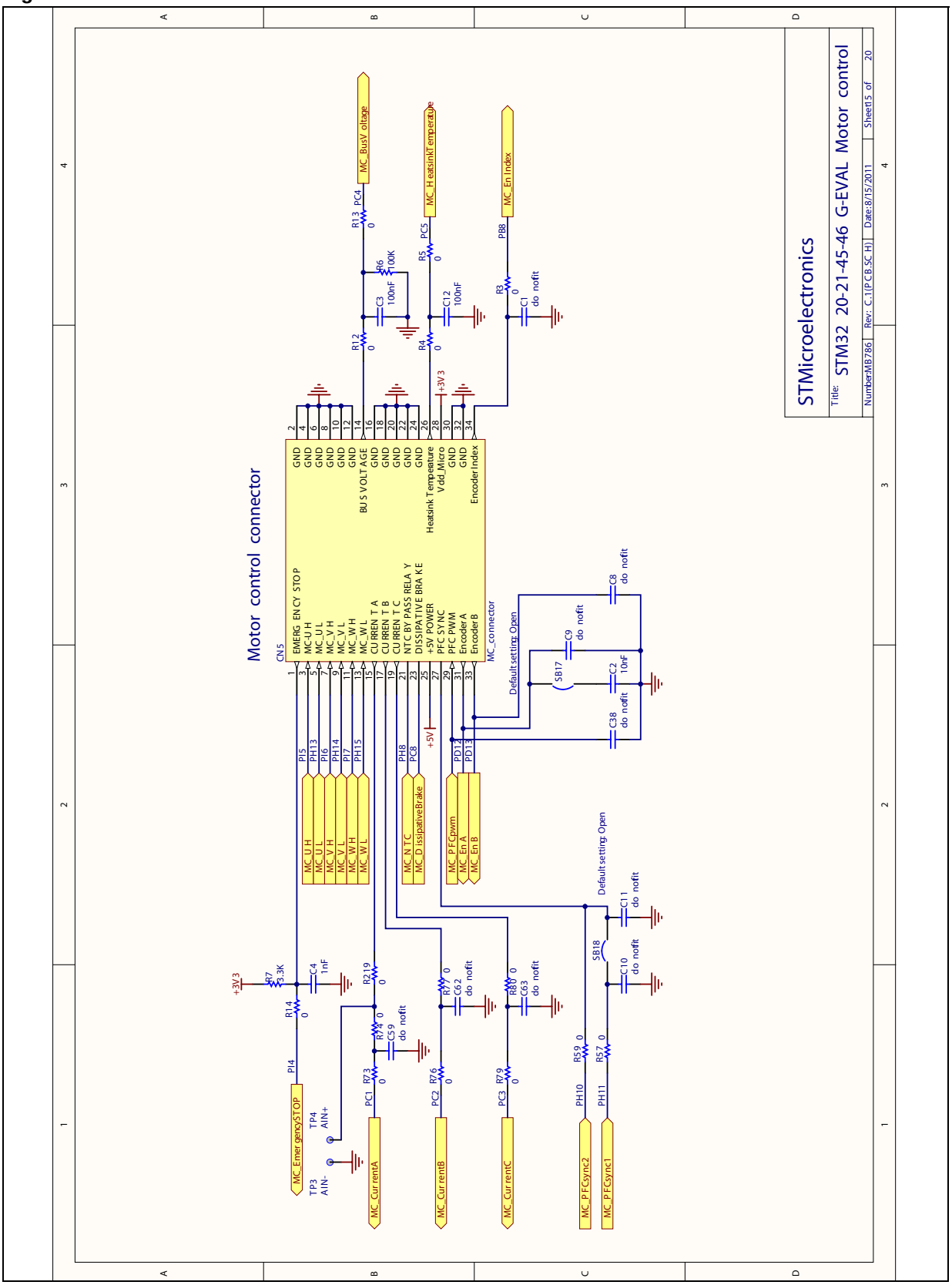


Figure 32. Smartcard

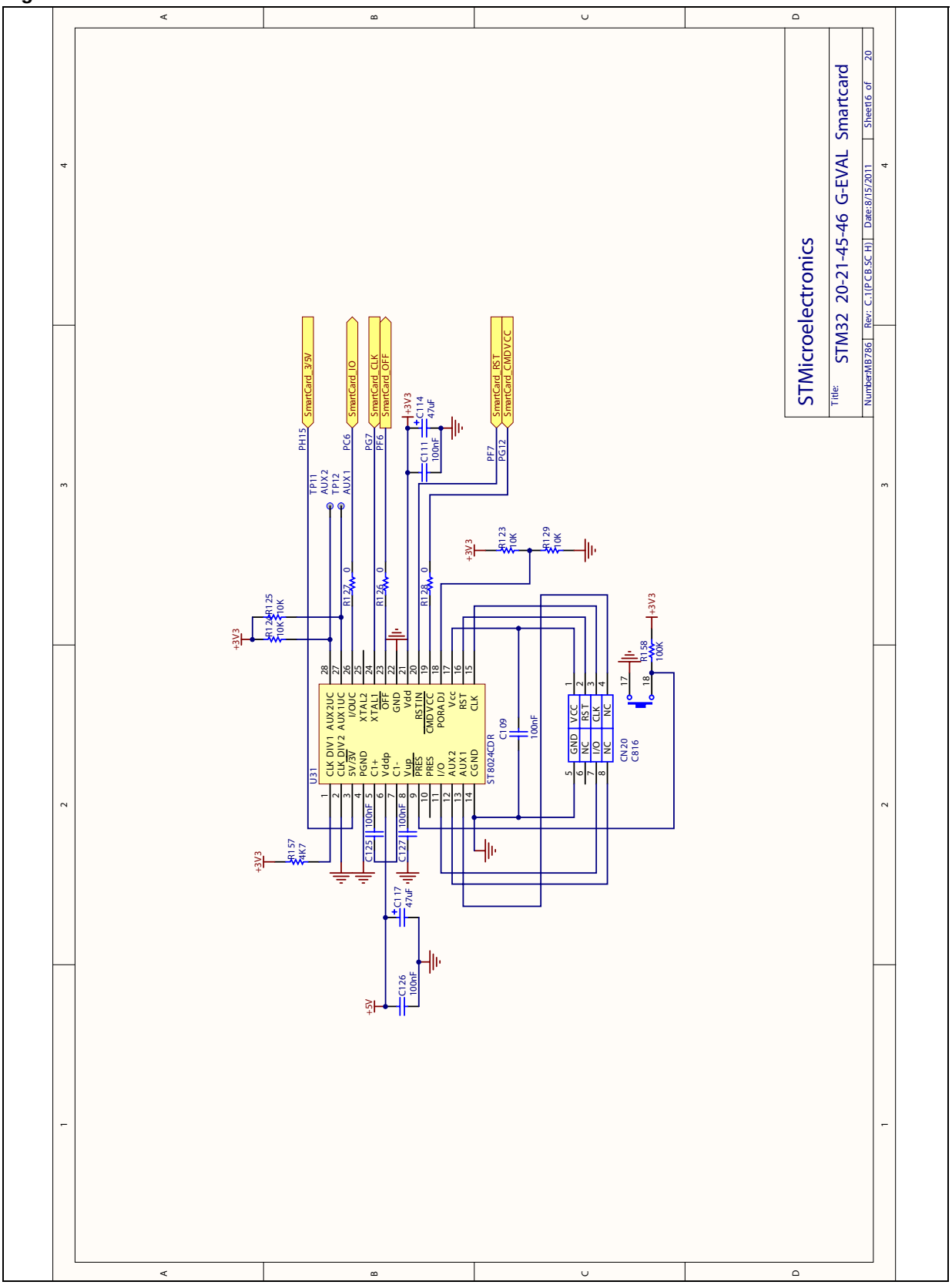


Figure 33. JTAG and Trace

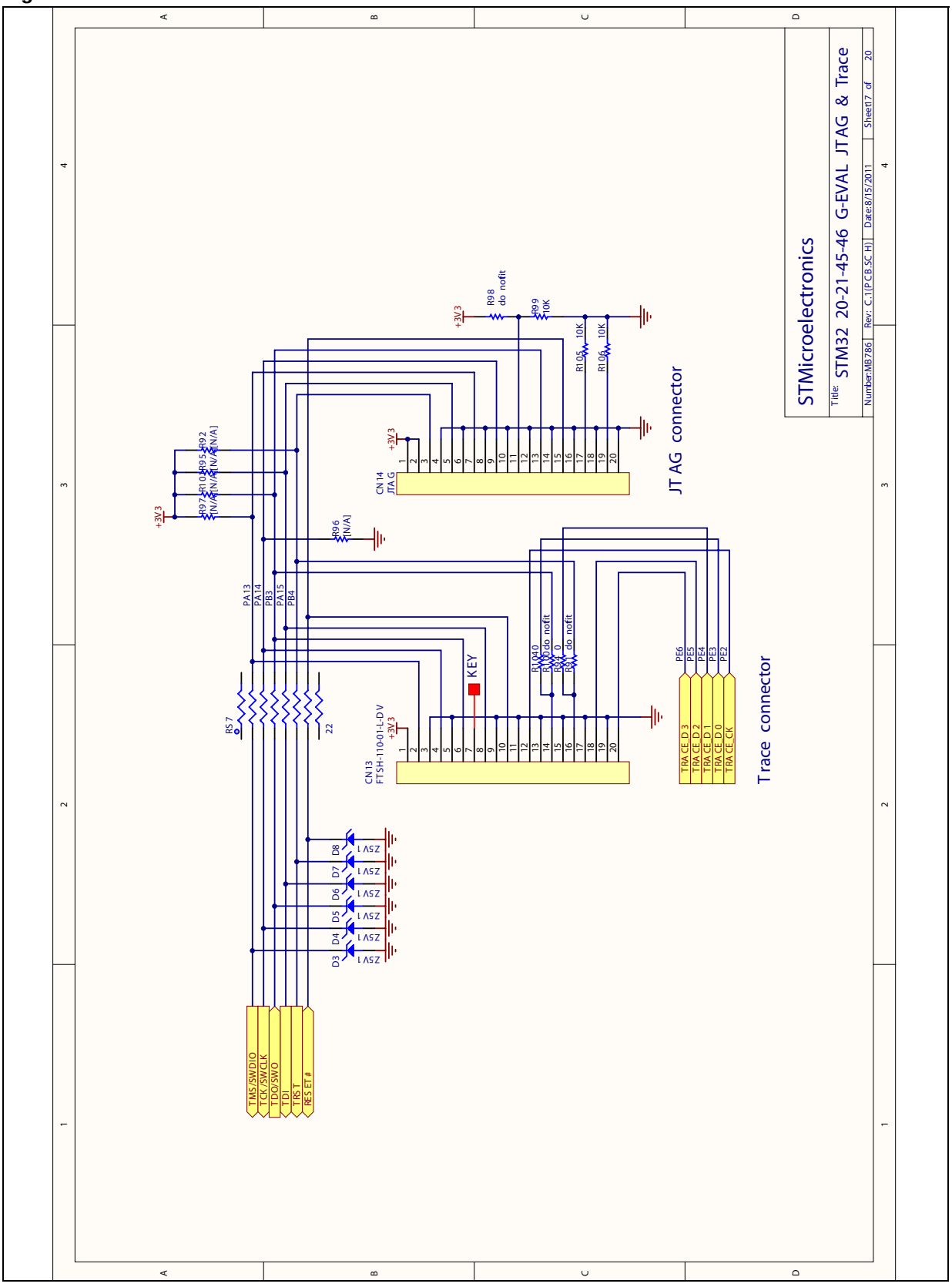


Figure 34. Power

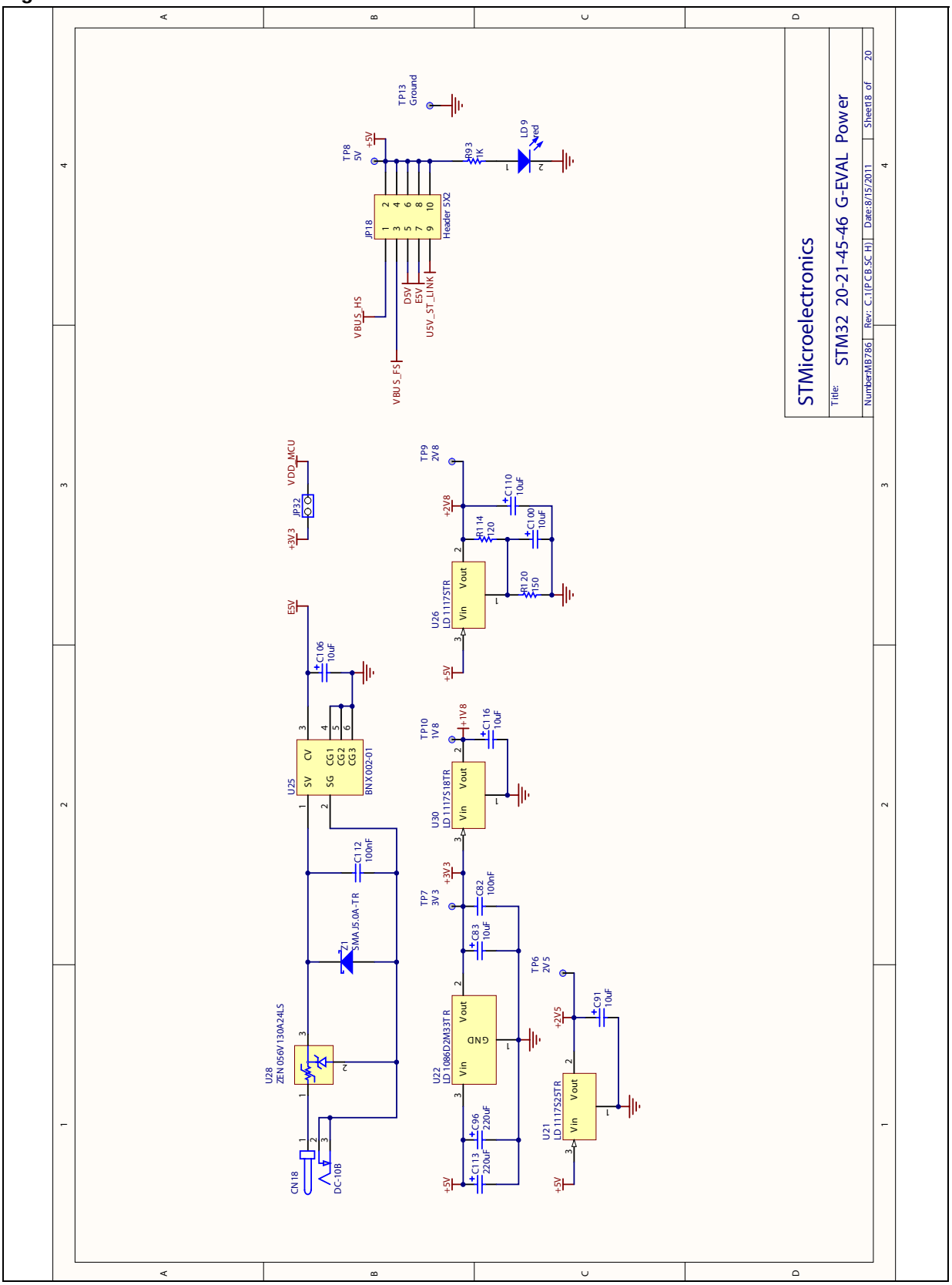


Figure 35. Extension connector

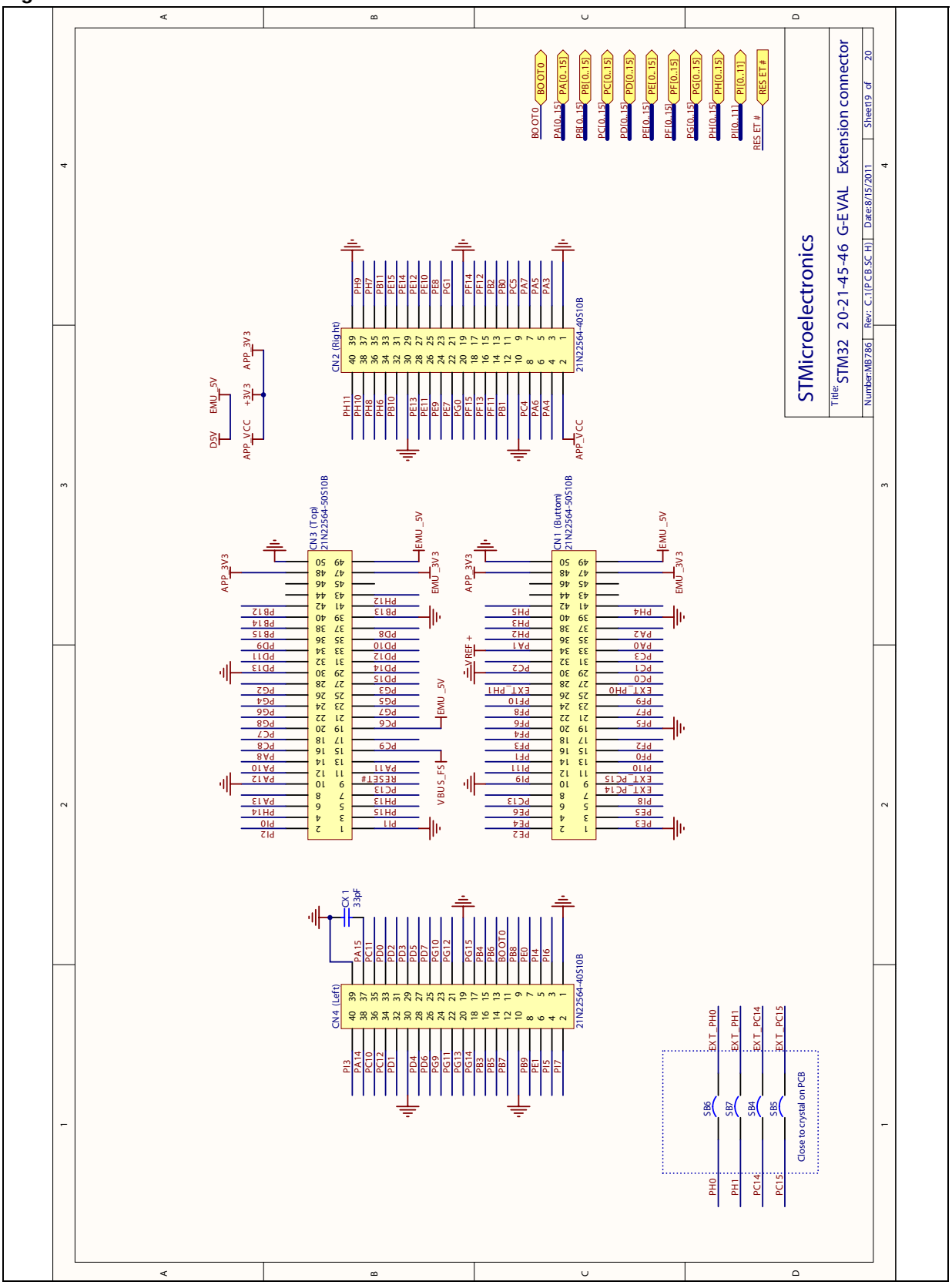


Figure 36. STLINK/V2

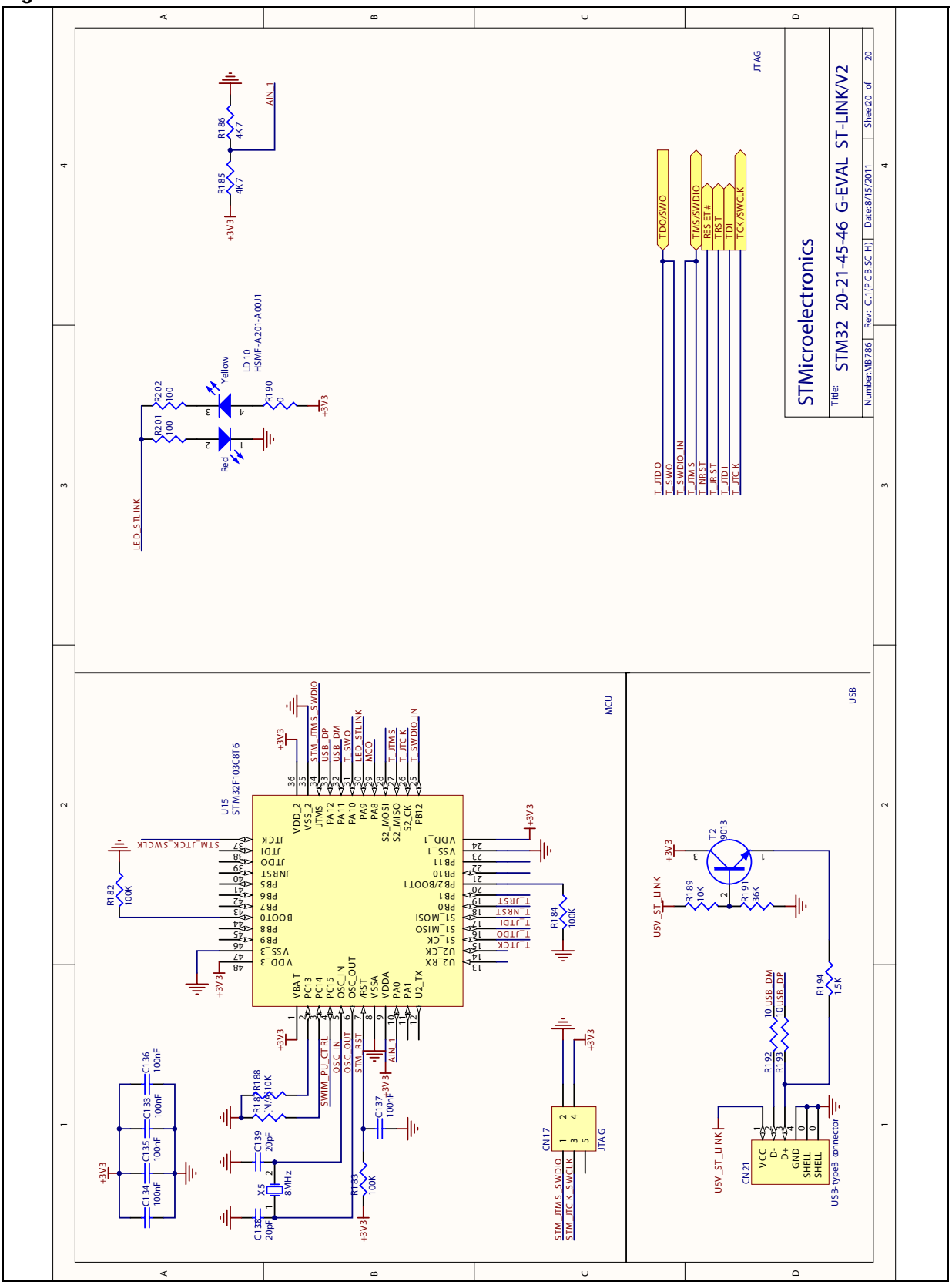
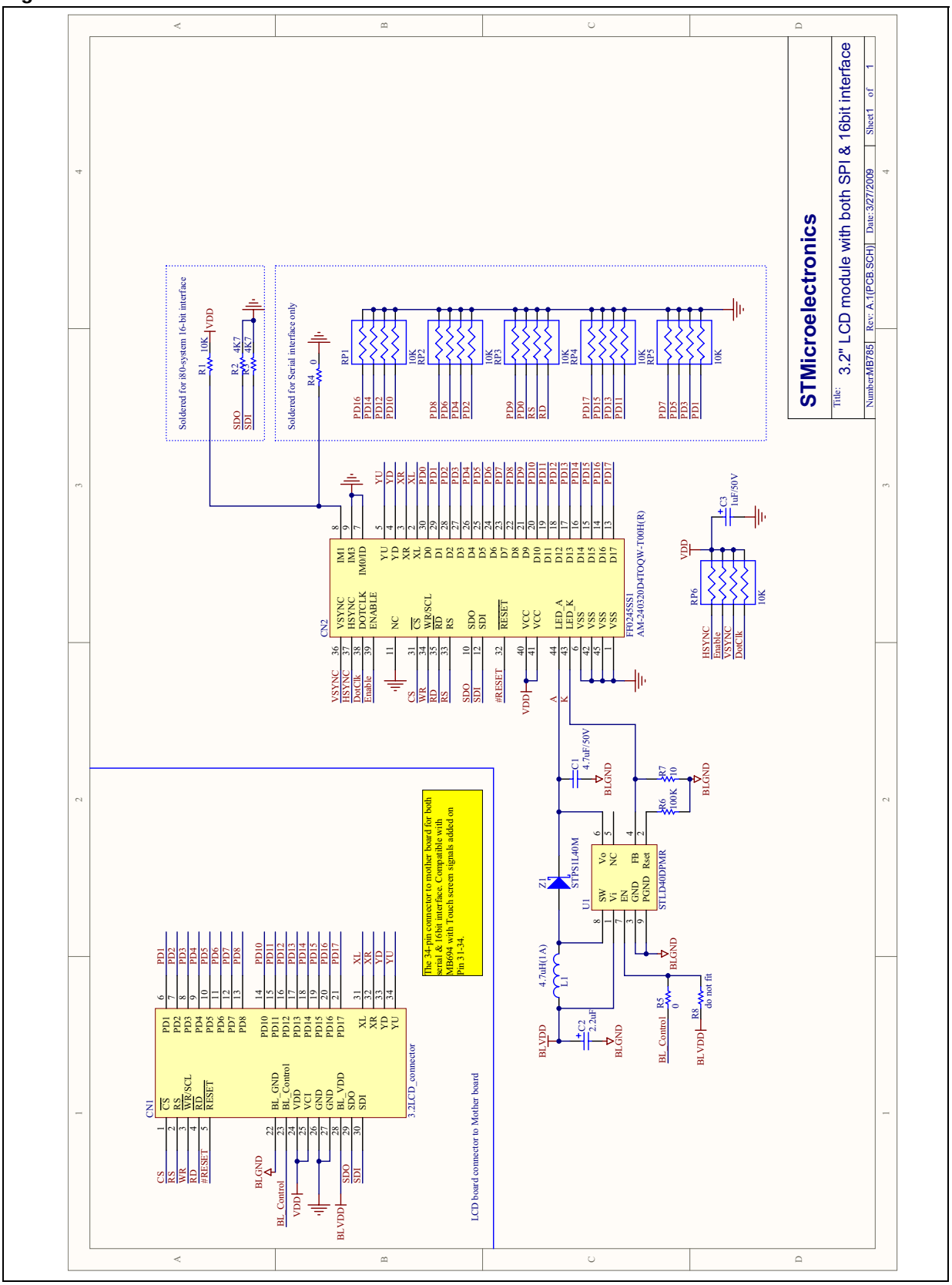


Figure 37. 3.2” LCD module with SPI and 16-bit interface



5 Revision history

Table 34. Document revision history

Date	Revision	Changes
02-Sept-2011	1d	Initial release.

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